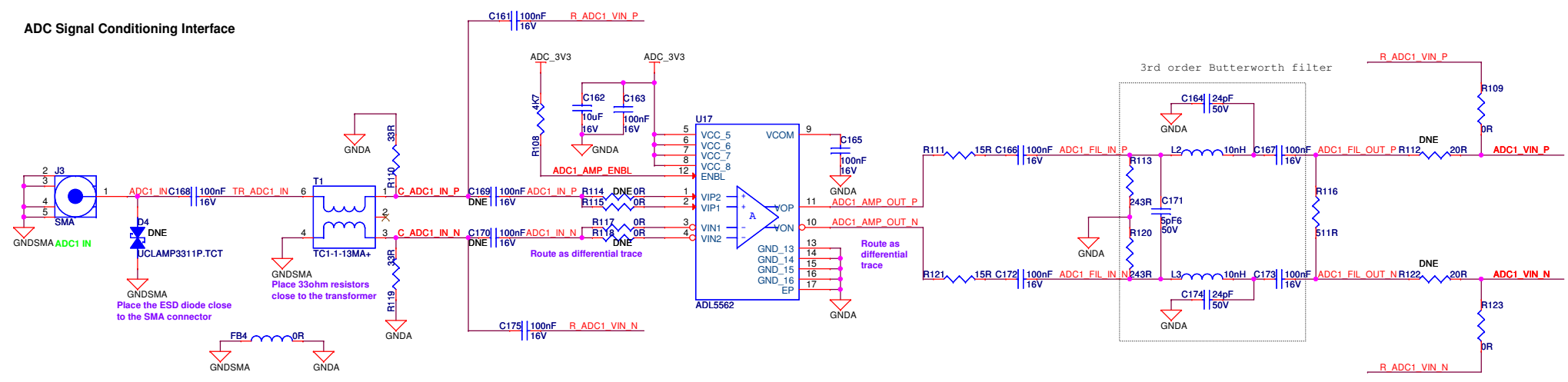
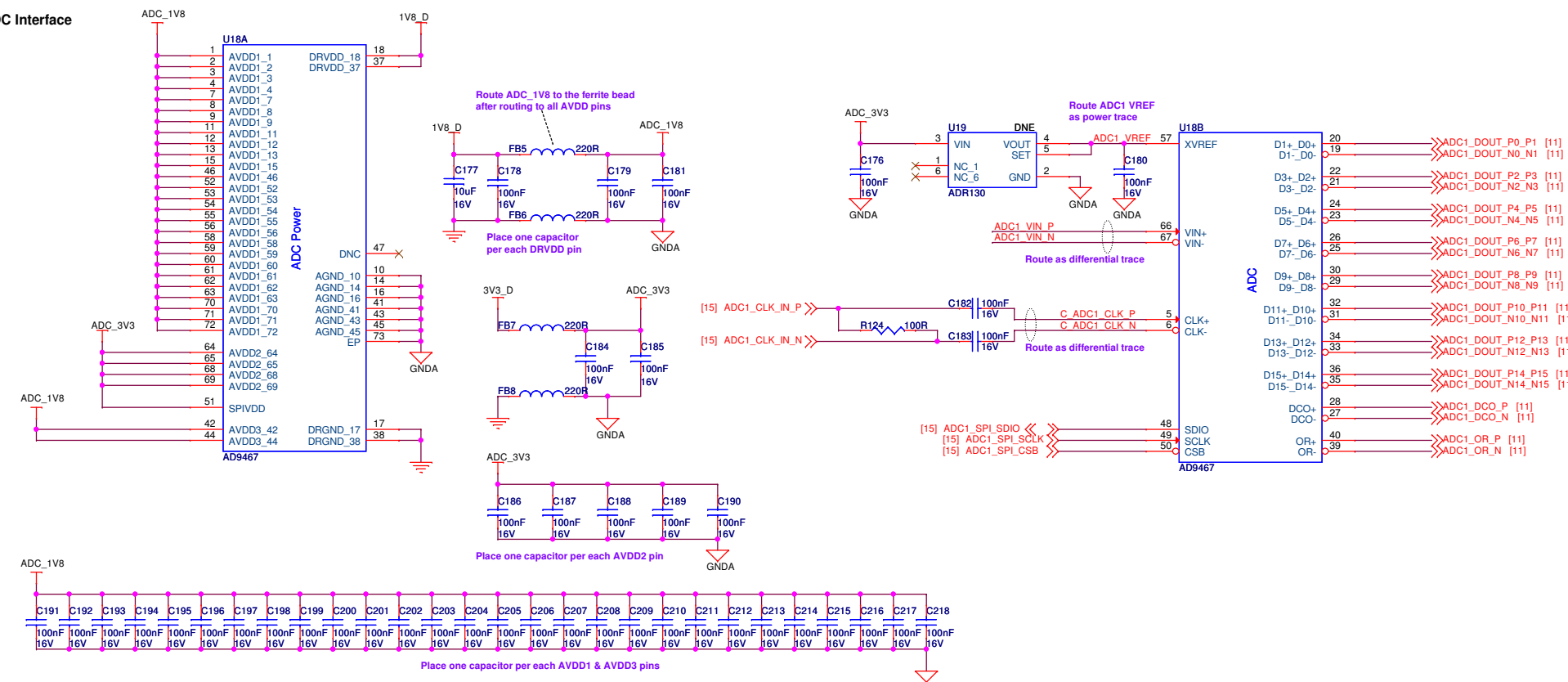


ADC1 INTERFACE

ADC Signal Conditioning Interface



ADC Interface



FPGA section

