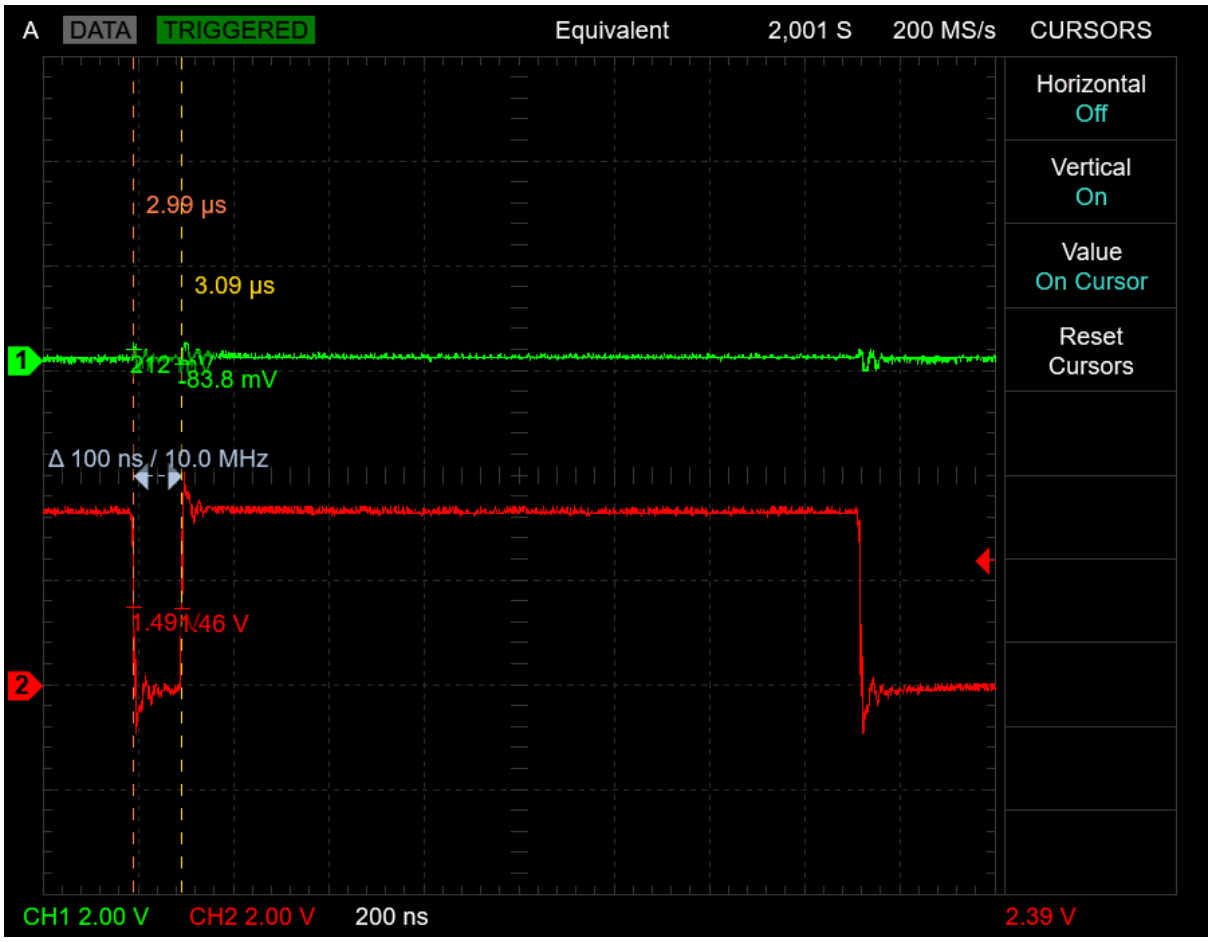
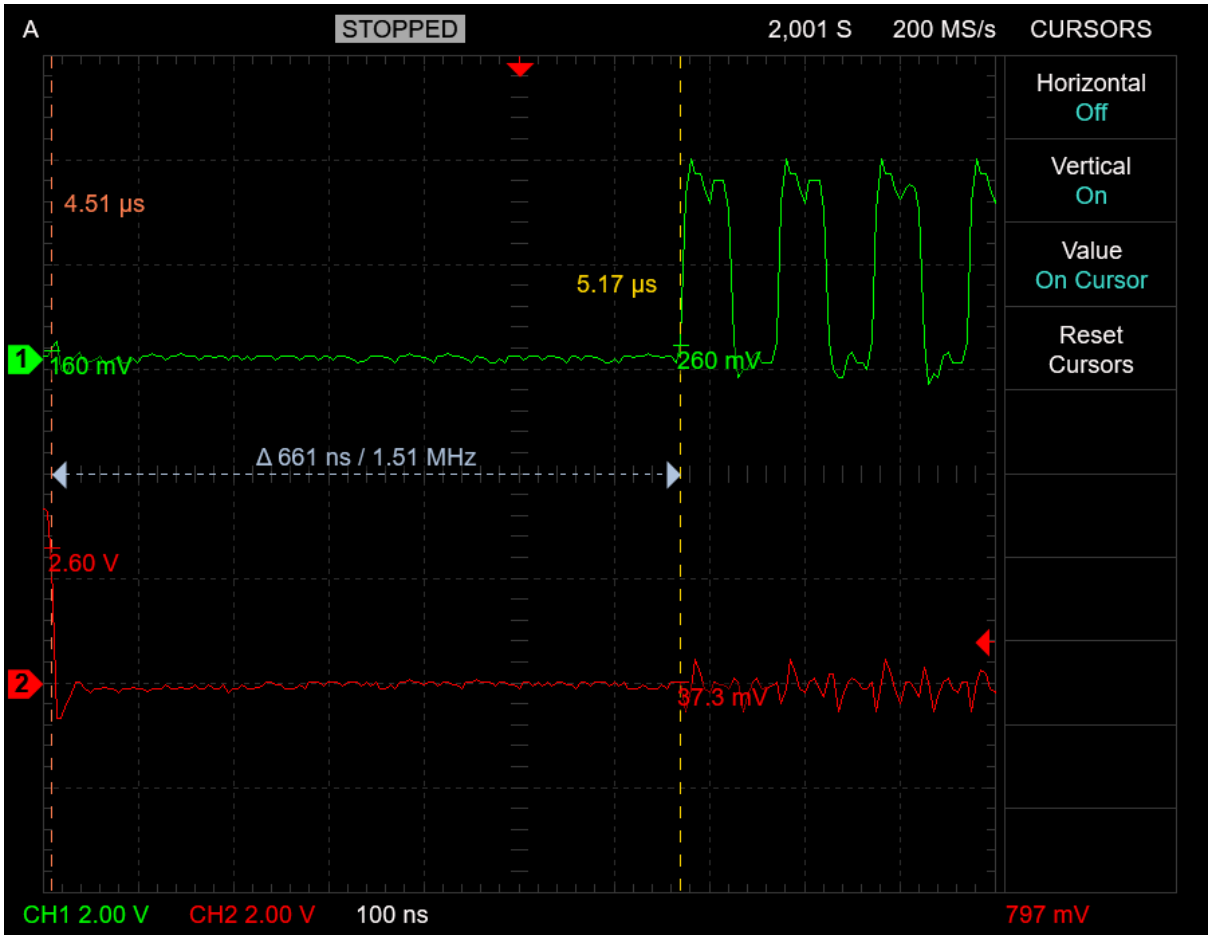
SPI set to 10MHz

Red = CONV, Green = SCK. Bring CONV LOW for 100ns, SCK remains LOW

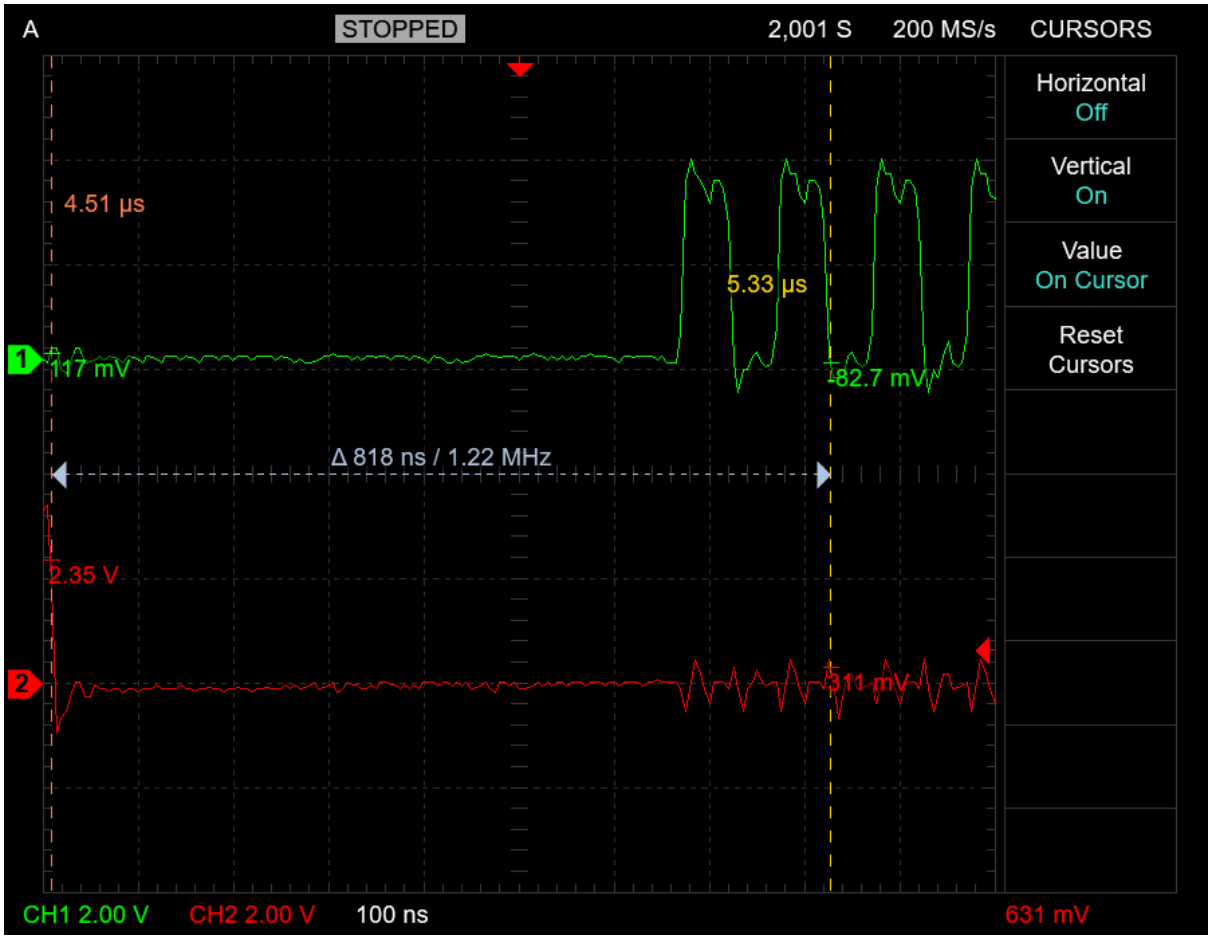


Red = CONV, Green = SCK. Bring CONV HIGH for >247ns (here 1.42us), SCK remains LOW

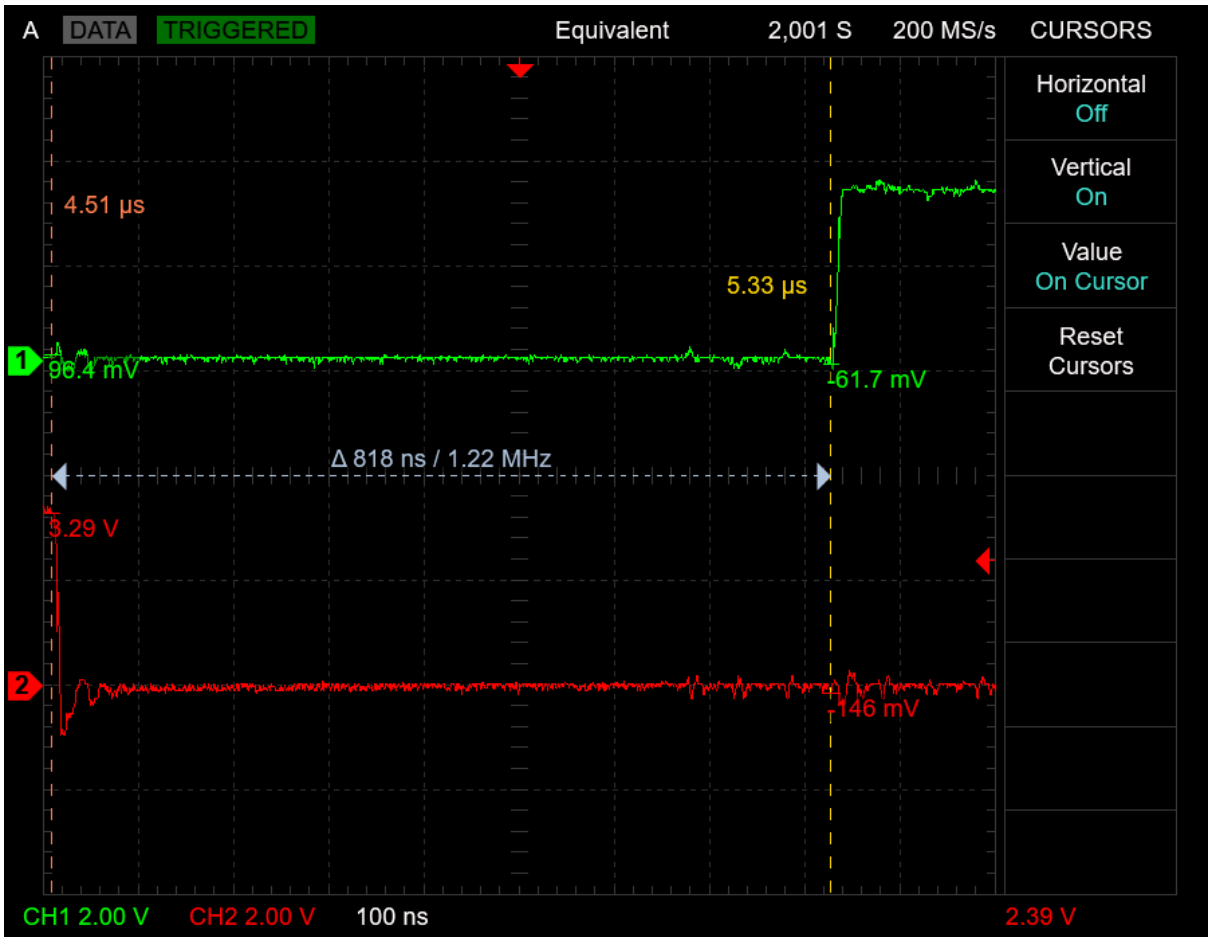
Red = CONV, Green = SCK. Leave CONV LOW, transfer 16 bits from ESP, creating 16 SCK pulses, start SCK 661ns after CONV = LOW



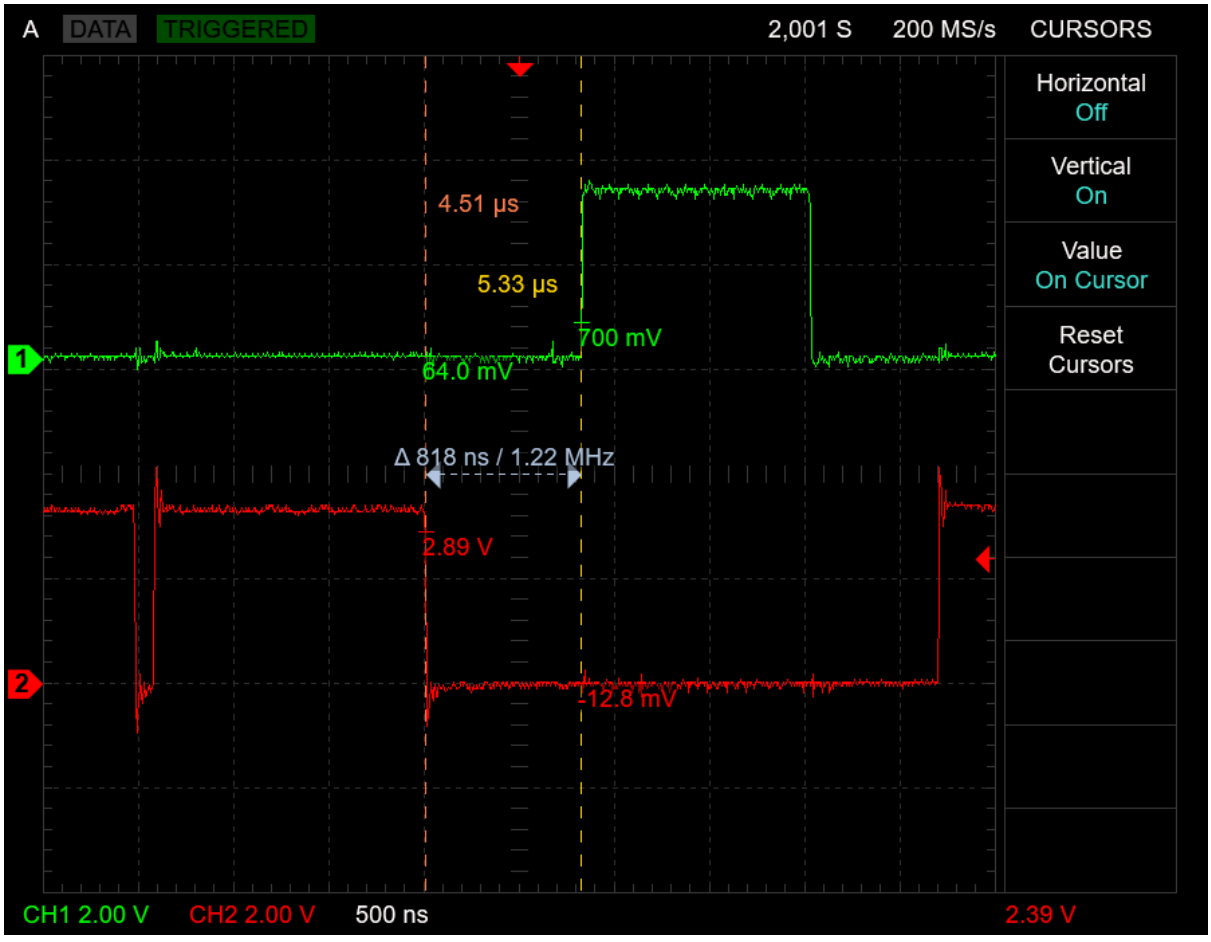
Red = CONV, Green = SCK. Cursor set on Falling edge second SCK (see further)



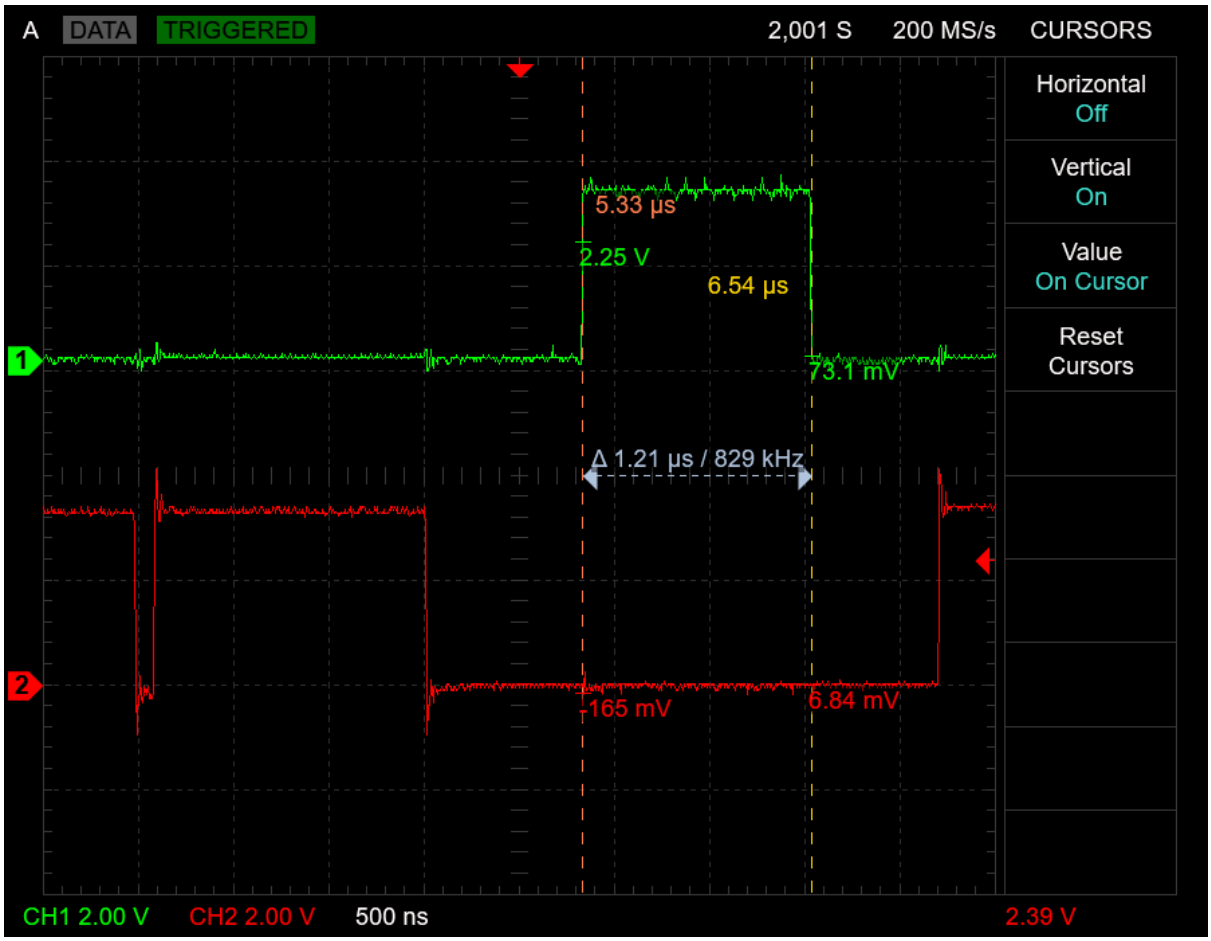
Red = CONV, Green = SDO. Analog in > Vref = 4095 output. Output SDO starts on falling edge SCK 2



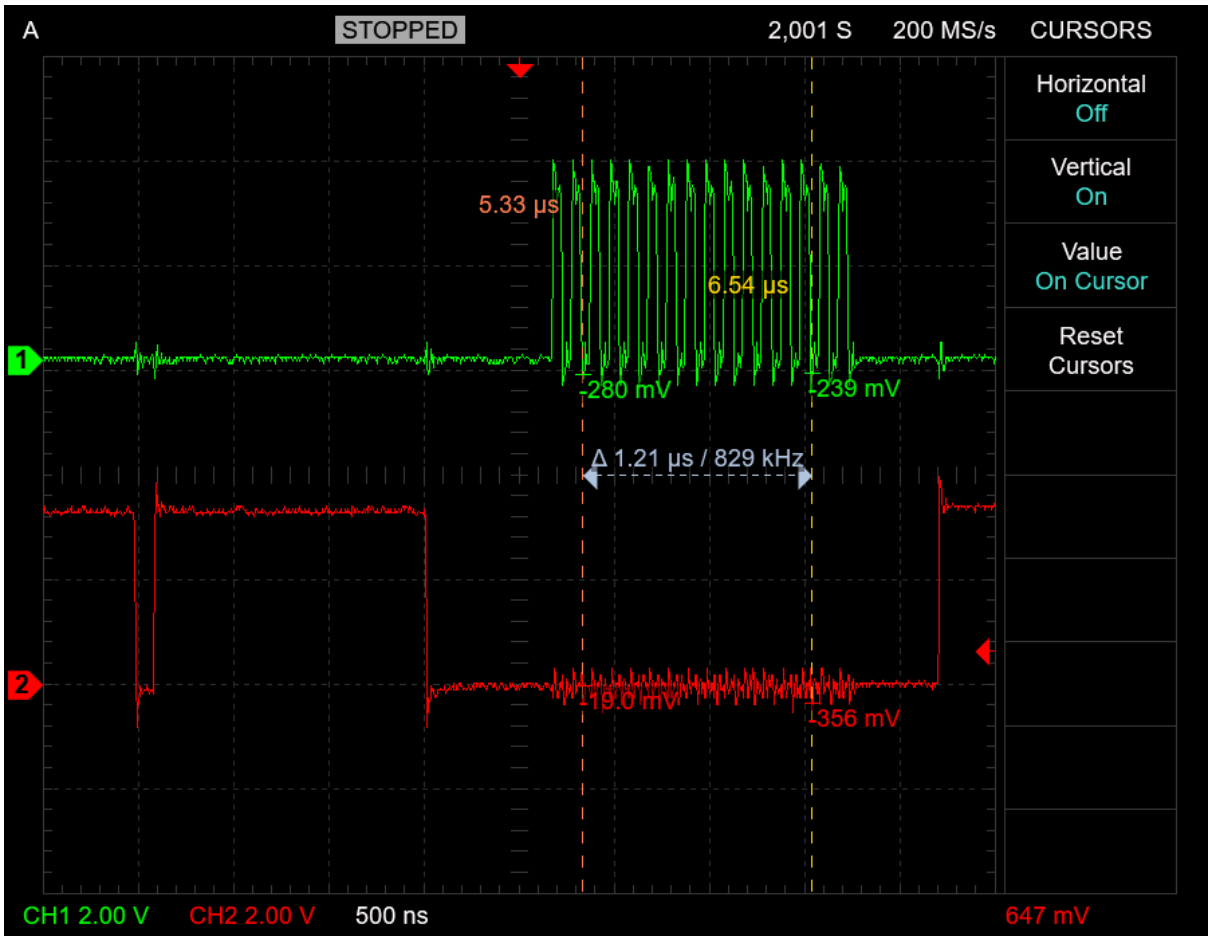
Red = CONV, Green = SDO. Analog in > Vref = 4095 output. Here we see SDO = 111111111111 (Binary 4095)



Red = CONV, Green = SDO. Analog in > Vref = 4095 output. Set cursors on MSB and LSB



Red = CONV, Green = SCK. Analog in > Vref = 4095 output. SDO LSB on falling edge of SCK 14



Red = CONV, Green = SCK. Analog in > Vref = 4095 output. Detail on the above