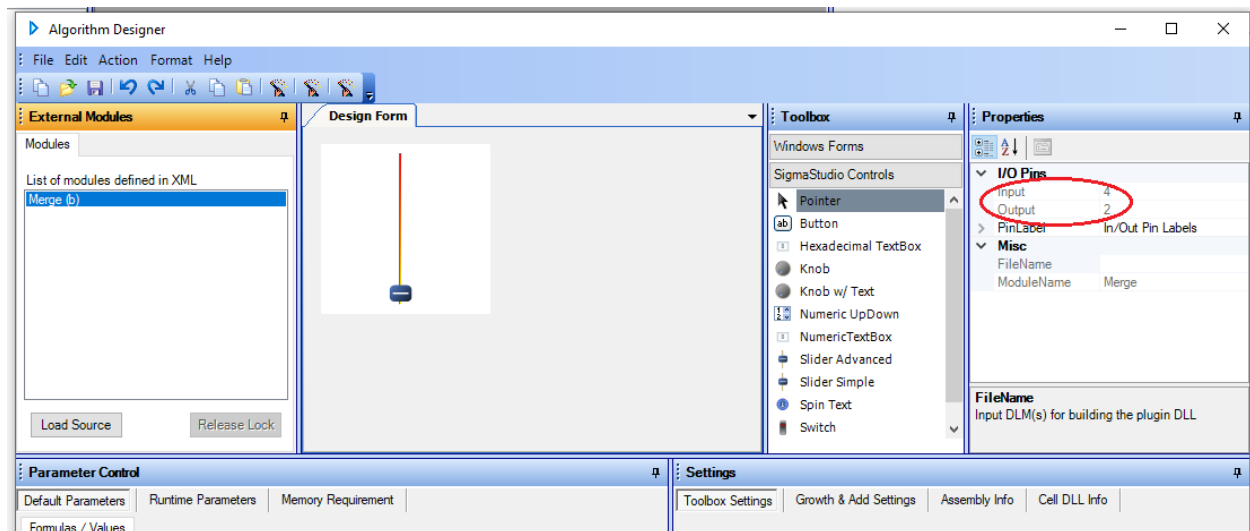


1. We can configure the inputs and outputs in xml file as shown in the below image.

```
adi_scaler_sc5xx.xml
1 <?xml version="1.0" standalone="yes"?>
2 <SS4SH name="ss4sh_module_xml" description="SigmaStudio for SHARC Algorithm Designer" version="0.0.0.1">
3   <module name="Merge" block="TRUE">
4     <dlb name="MergeCh.dlb" target="ADSP-SC58x" embed="TRUE" path="C:\CHIRU\Docs\DLB"/>
5     <pin type="DATA" direction="INPUT" />
6     <pin type="DATA" direction="INPUT" />
7     <pin type="DATA" direction="INPUT" />
8     <pin type="DATA" direction="INPUT" />
9     <pin type="DATA" direction="OUTPUT" />
10    <pin type="DATA" direction="OUTPUT" />
11  </module>
12 </SS4SH>
```

2. After adding xml file with above settings into algorithm designer, we can see the pin information in the designer window as shown below.



3. Please use **nInputs** for inChs and **nOutputs** for outChs in the C code which you shared.

```
void BPROCESS_Merge (SSBlockAlgo* pBlkAlgoInfo)
```

```
{
  int blockSize, outChs, inChs;
  int out_ch,in_ch,sample=0;
  float *pInput, *pOutput;

  inChs =pBlkAlgoInfo->nInputs;

  outChs = pBlkAlgoInfo->nOutputs;
```

```
for(out_ch = 0; out_ch < outChs; out_ch++)  
  
{  
    pOutput = pBlkAlgoInfo->pOutputs[out_ch].pSamples;  
  
    blockSize = pBlkAlgoInfo->pInputs[out_ch].pBlockProperties->nBlockSize;  
  
    for(sample = 0; sample < blockSize; sample++)  
  
    {  
        for (in_ch = 0; in_ch < inChs; in_ch++)  
  
        {  
            pInput = pBlkAlgoInfo->pInputs[in_ch].pSamples;  
  
            if(in_ch==0)  
            {  
                pOutput[sample] = pInput[sample];  
            }  
            else  
            {  
                pOutput[sample] += pInput[sample];  
            }  
        }  
    }  
}  
}
```