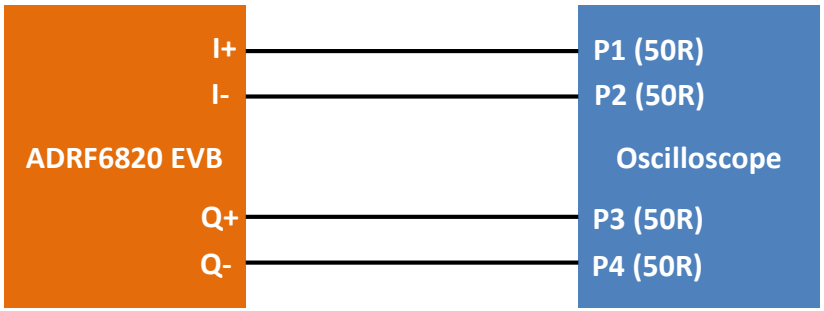
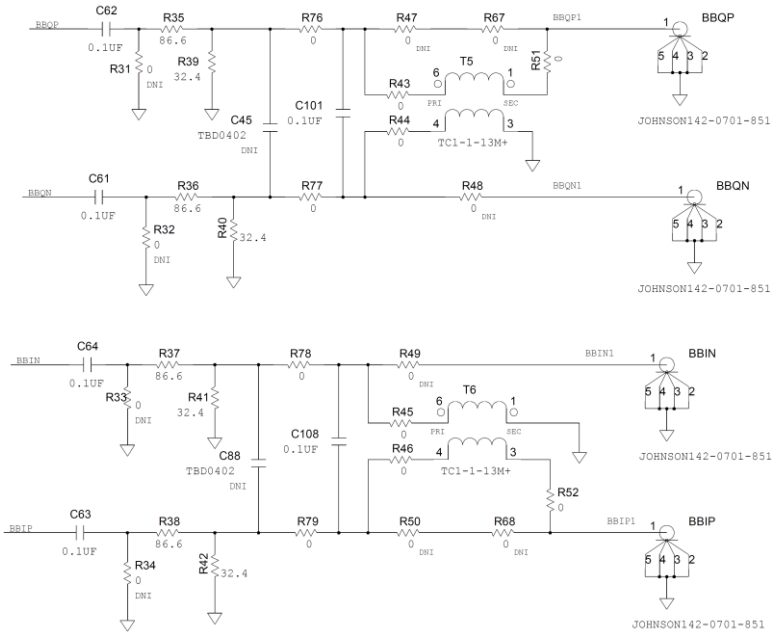
Test 1 (1/3)

HW Settings :

1. I path and Q path connected with oscilloscope directly.
   - R35/R36/R37/R38 changed to 0R
   - R39/R40/R41/R42 changed to DNI
   - C45/C88/C101/C108 changed to DNI
   - R43/R44/R45/R46/R51/R52 changed to DNI
   - R47/R48/R49/R50/R67/R68 changed to 0R
2. Oscilloscope set to 50R for each port.
3. RF (RF0 port) : 1960MHz, -1.95dBm
   LO (LO IP port) : 1950MHz, -1.25dBm

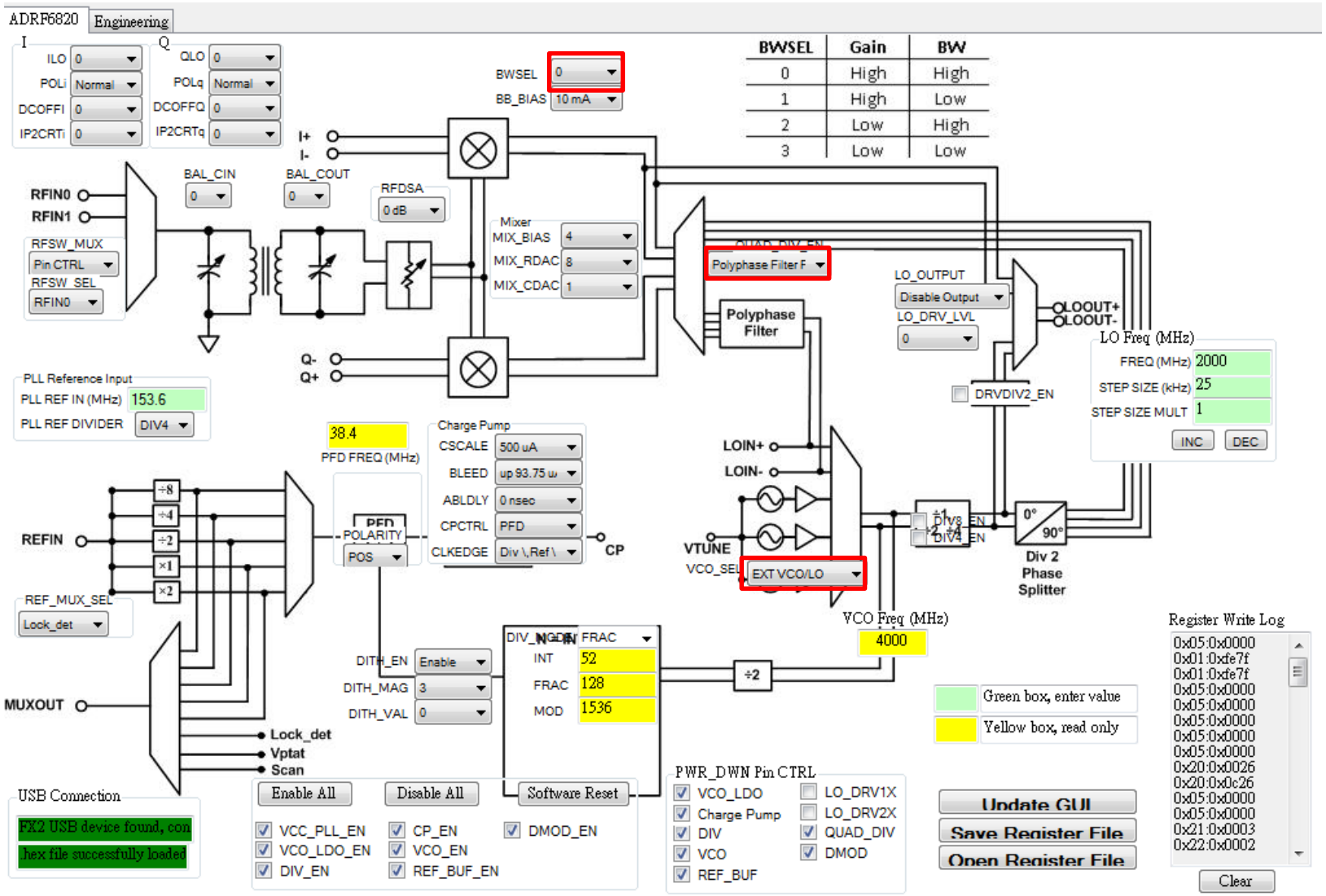




Test 1 (2/3)

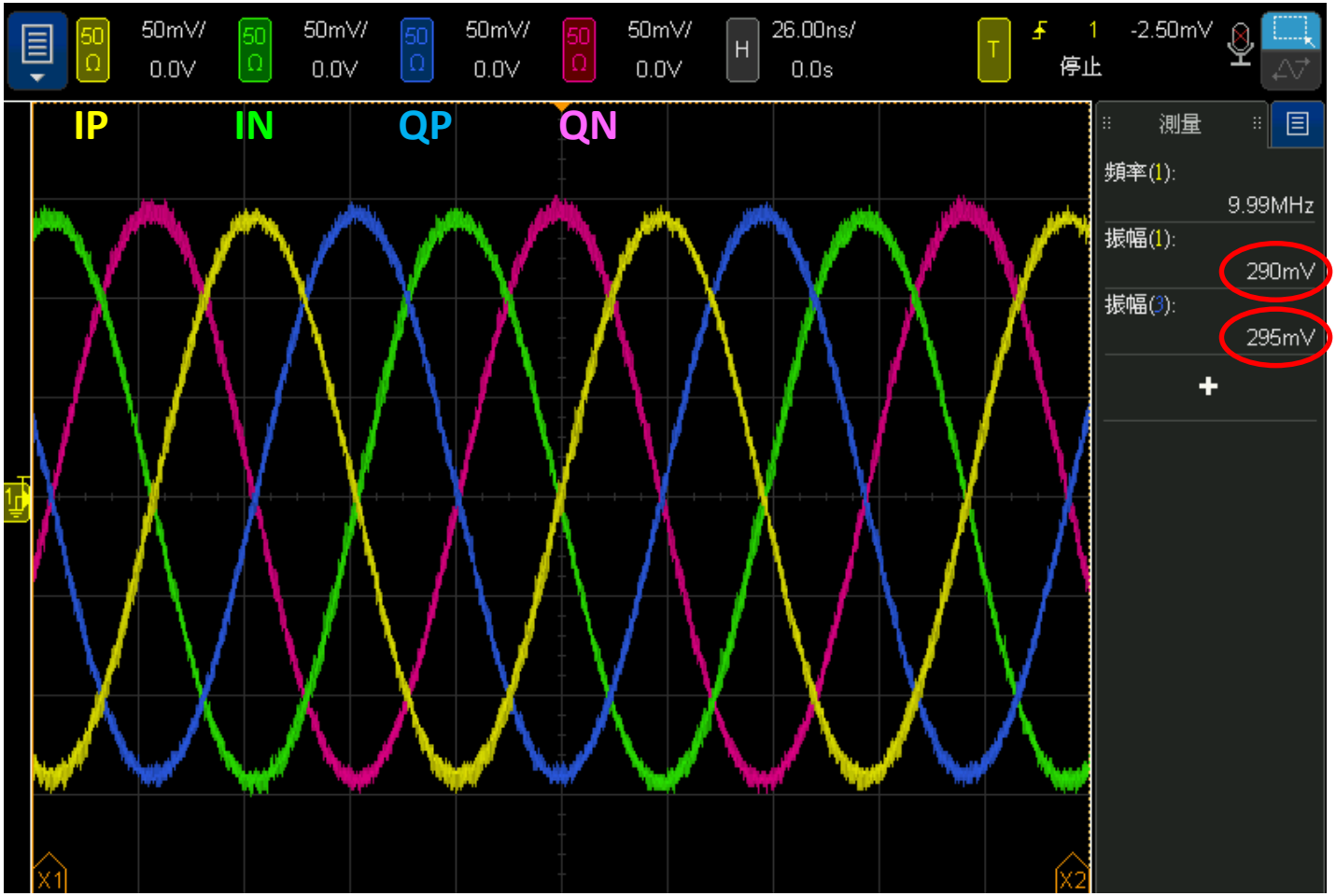
SW Settings :

1. Control SW set to “EXT VCO/LO”, “Polyphase Filter” and “BWSEL=0”.



Test 1 (3/3)

IQ Output



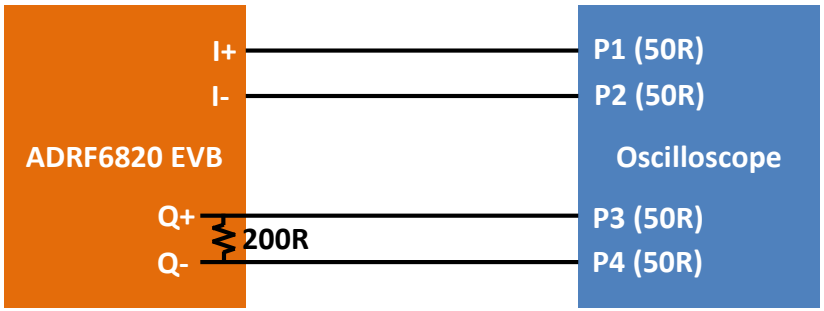
→ -6.77dBm

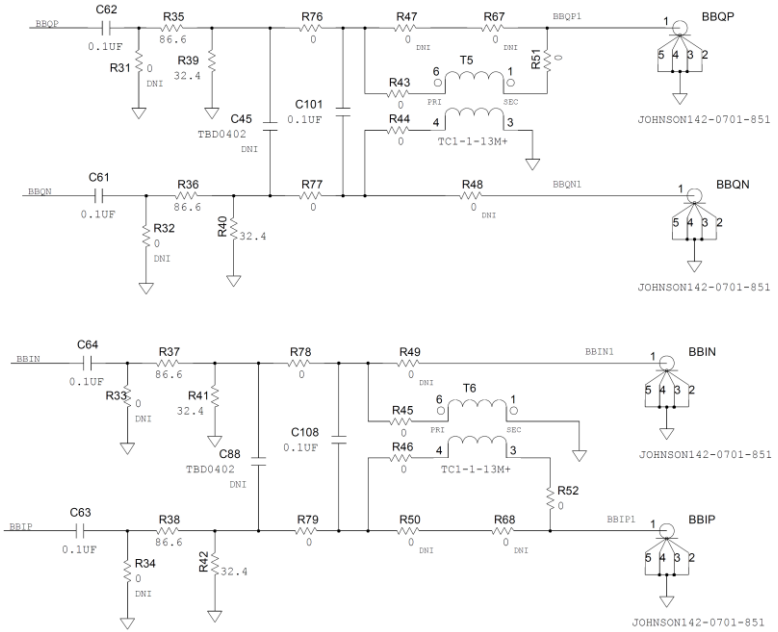
→ -6.63dBm

Test 2 (1/3)

HW Settings :

1. I path connected with oscilloscope directly and Q path added a parallel 200R.
   - R35/R36/R37/R38 changed to 0R
   - R39/R40/R41/R42 changed to DNI
   - C88 changed to 200R, C45/C101/C108 changed to DNI
   - R43/R44/R45/R46/R51/R52 changed to DNI
   - R47/R48/R49/R50/R67/R68 changed to 0R
2. Oscilloscope set to 50R for each port.
3. RF (RF0 port) : 1960MHz, -1.95dBm
   LO (LO IP port) : 1950MHz, -1.25dBm

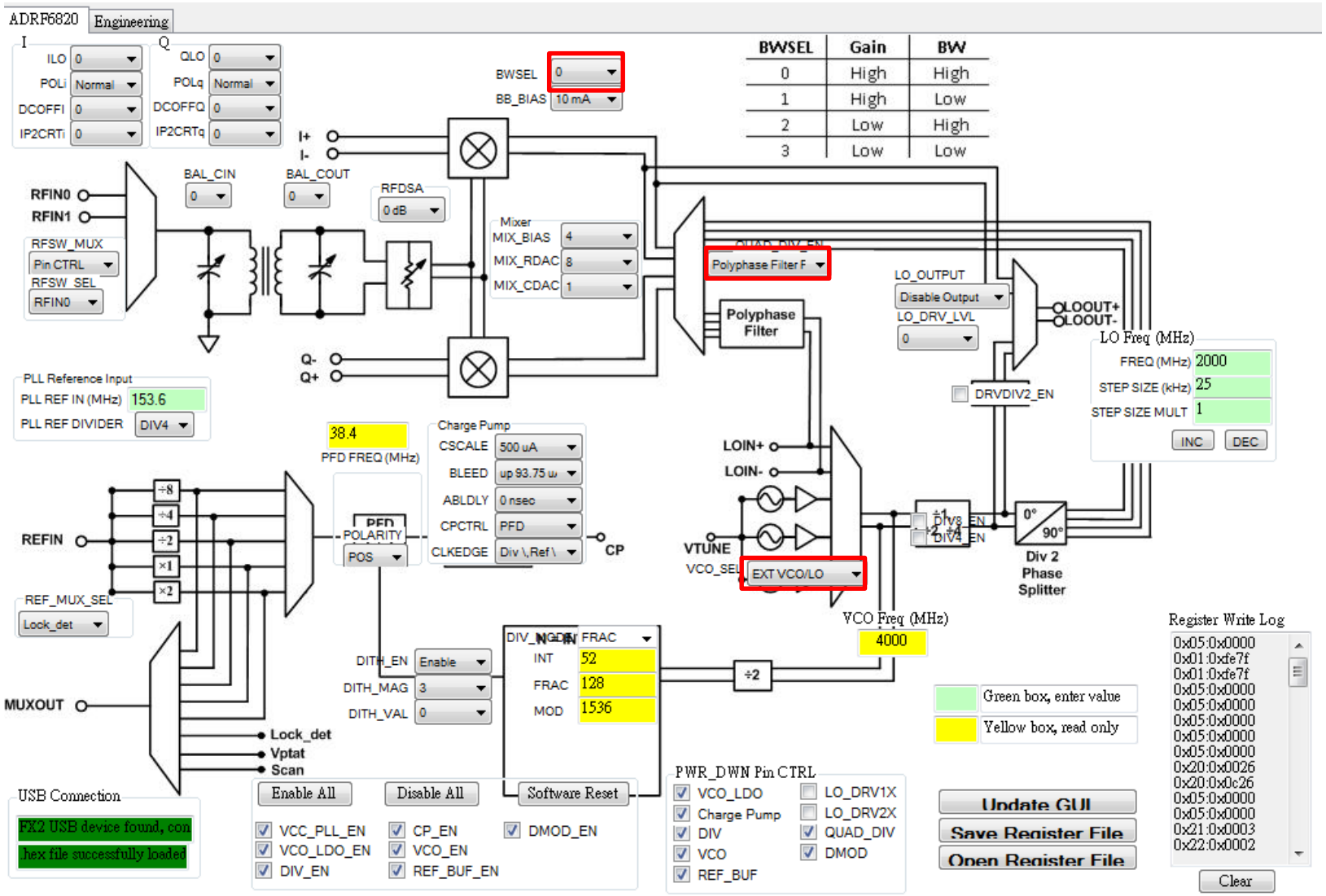




Test 2 (2/3)

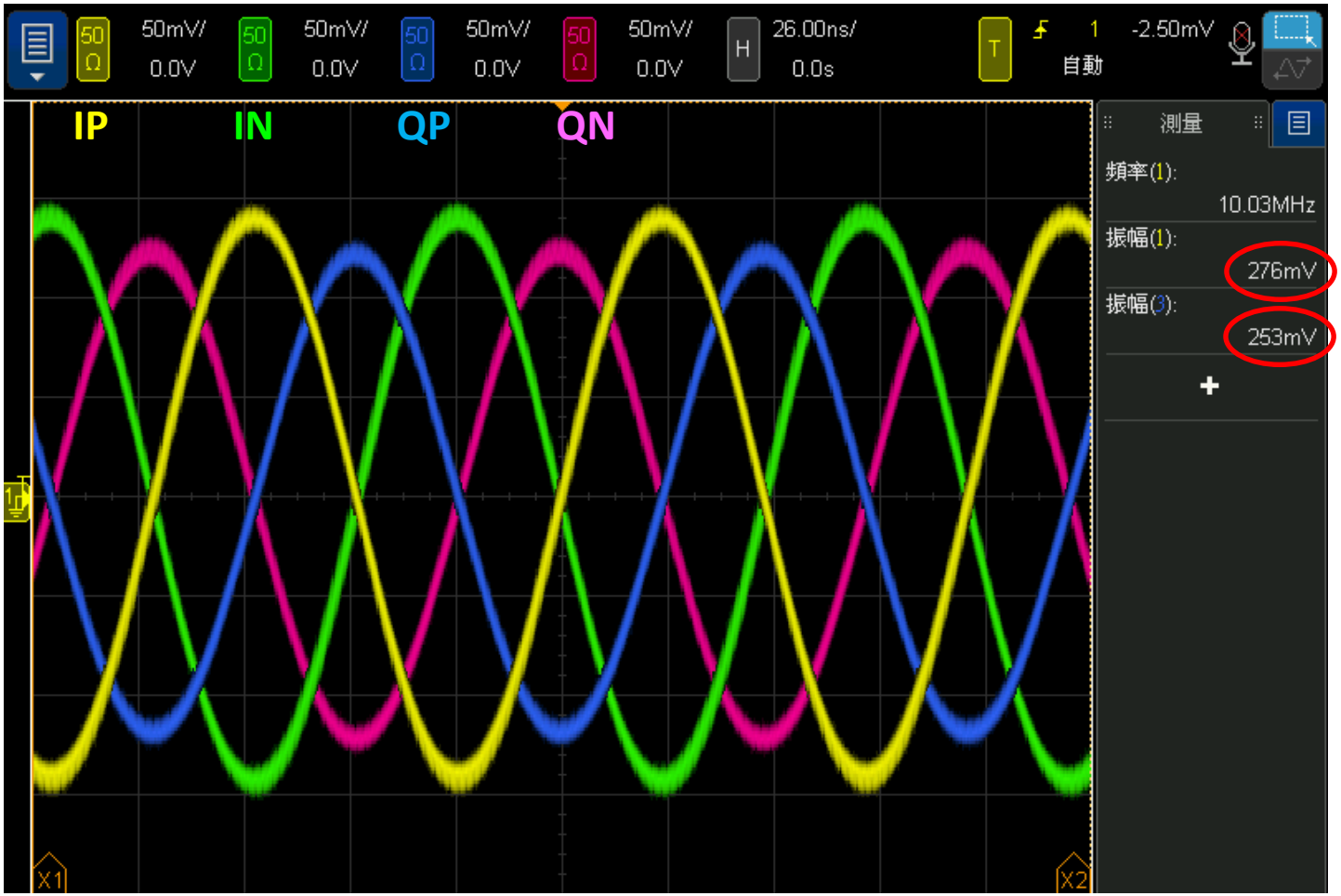
SW Settings :

1. Control SW set to “EXT VCO/LO”, “Polyphase Filter” and “BWSEL=0”.



Test 2 (3/3)

IQ Output



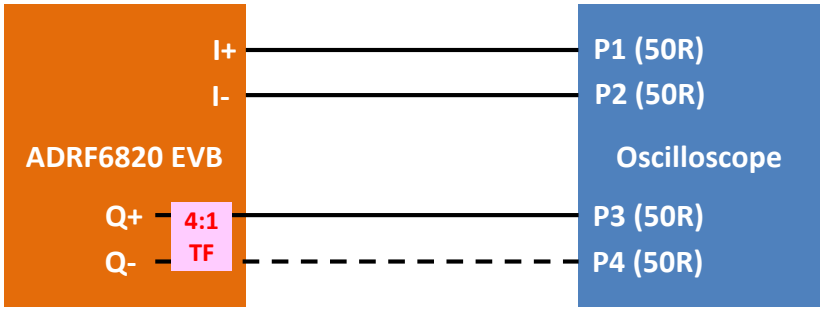
-7.20dBm

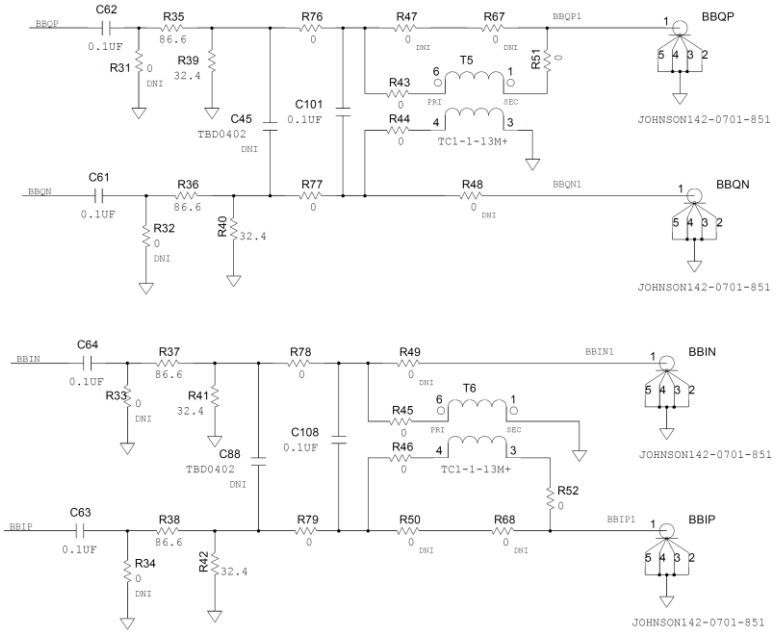
-7.96dBm

Test 3 (1/3)

HW Settings :

1. I path connected with oscilloscope directly and Q path added a 4:1 impedance transformer (TC4-1W+).
   - R35/R36/R37/R38 changed to 0R
   - R39/R40/R41/R42 changed to DNI
   - C45/C88/C101/C108 changed to DNI
   - T5 changed to TC4-1W+, T6 changed to DNI
   - R43/R44/R51 changed to 0R, R45/R46/R52 changed to DNI
   - R47/R48/R67 changed to DNI, R49/R50/R68 changed to 0R
2. Oscilloscope set to 50R for each port.
3. RF (RF0 port) : 1960MHz, -1.95dBm
   LO (LO IP port) : 1950MHz, -1.25dBm

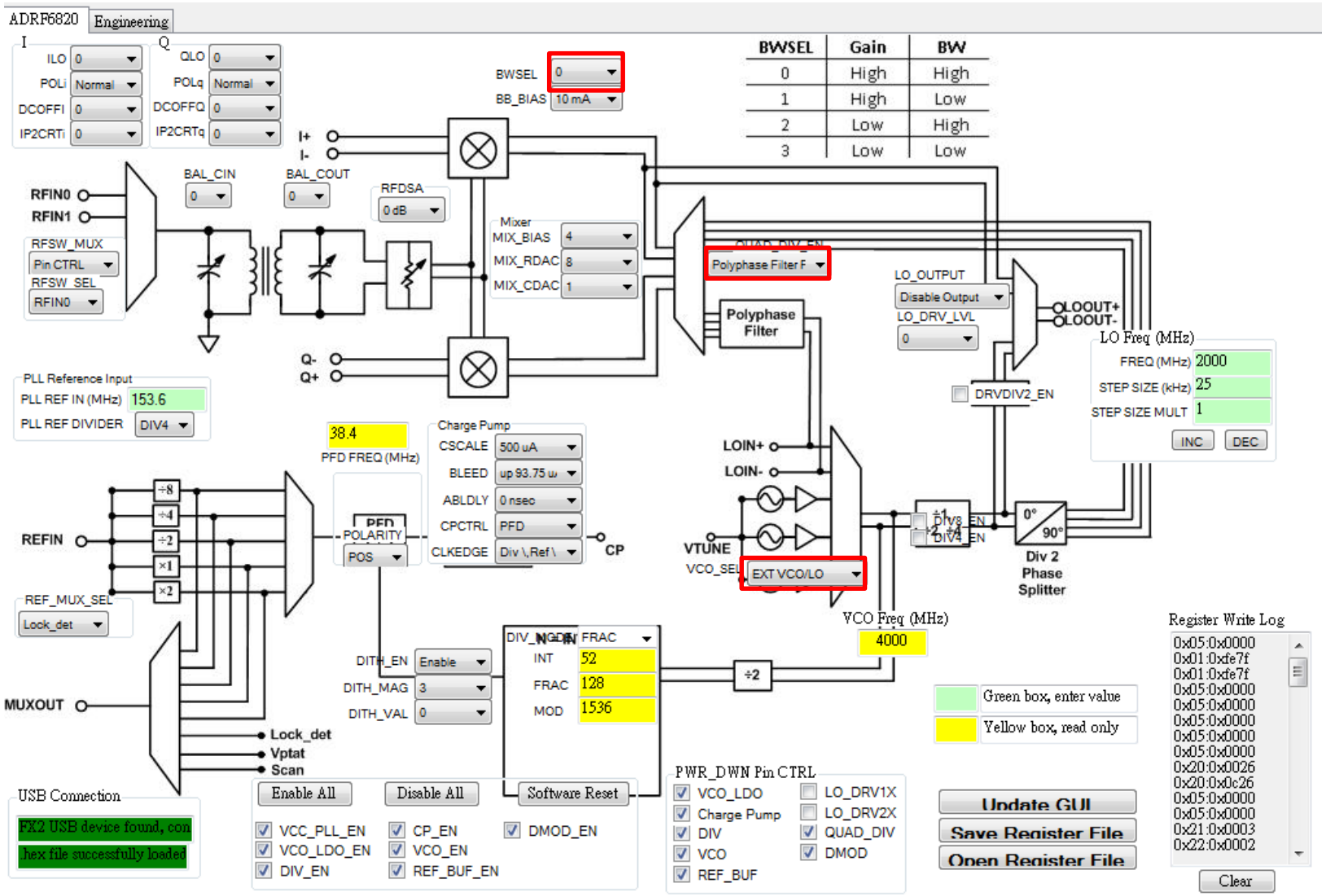




Test 3 (2/3)

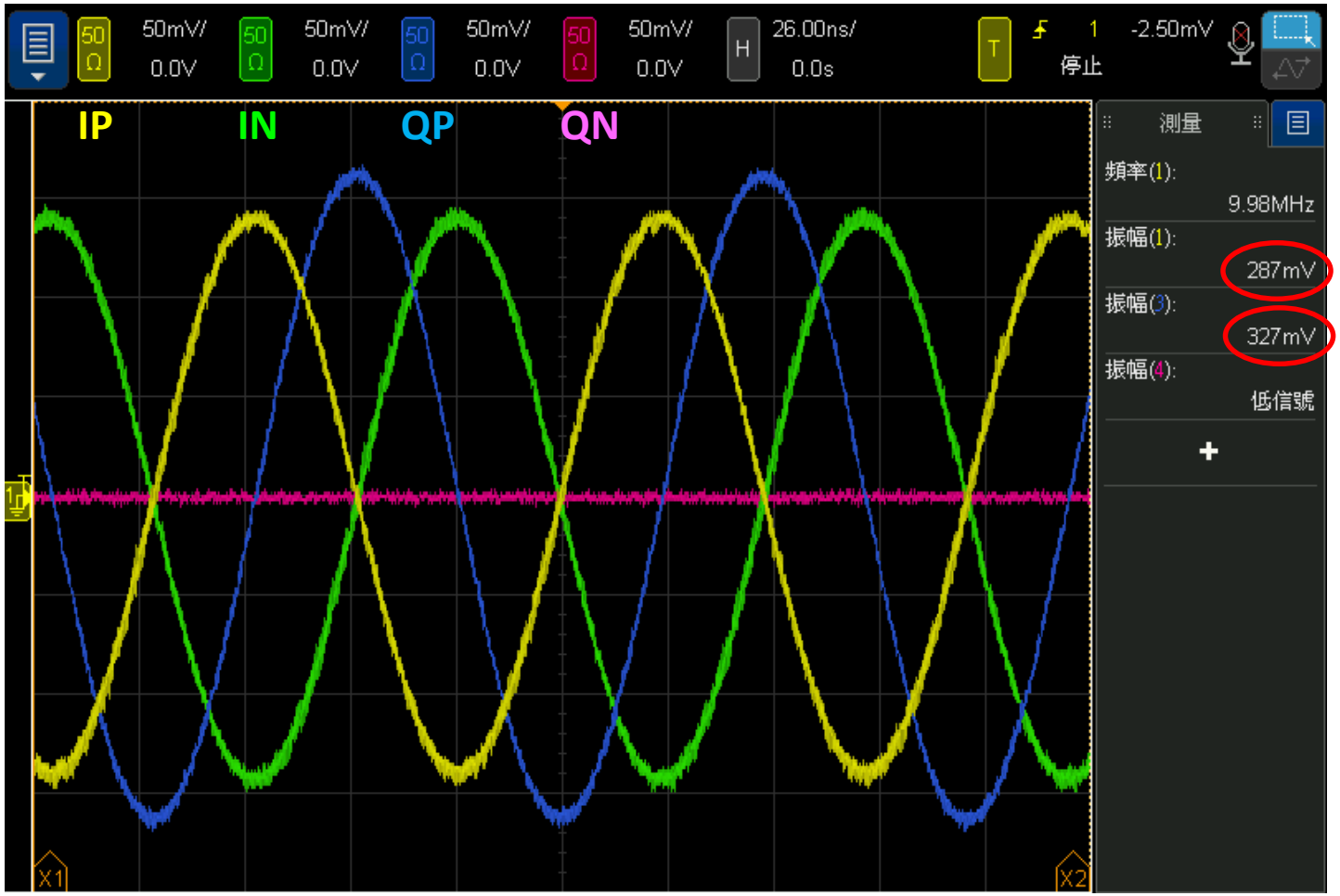
SW Settings :

1. Control SW set to “EXT VCO/LO”, “Polyphase Filter” and “BWSEL=0”.



Test 3 (3/3)

IQ Output



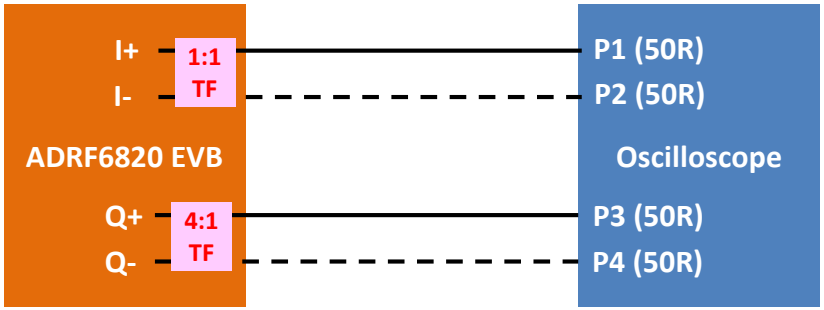
-6.86dBm

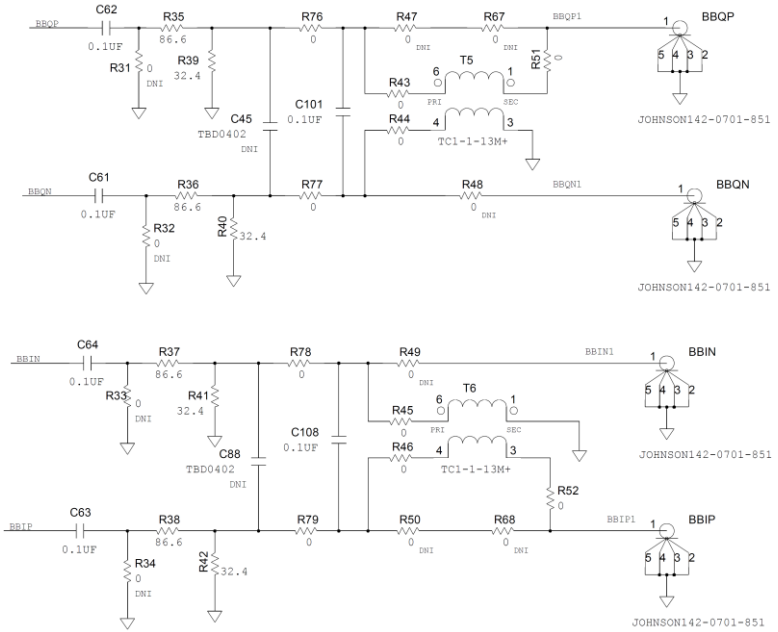
-5.73dBm

Test 4 (1/3)

HW Settings :

1. **I path returned to EVB setting (using 1:1 transformer TC1-1-13M+) and Q path used a 4:1 impedance transformer (TC4-1W+).**
   - **R35/R36 changed to 0R, R37/R38 changed to 86.6R**
   - **R39/R40 changed to DNI, R41/R42 changed to 32.4R**
   - **R43/R44/R45/R46/R51/R52 changed to 0R**
   - **R47/R48/R49/R50/R67/R68 changed to DNI**
   - **C45/C88/C101/C108 changed to DNI**
   - **T5 changed to TC4-1W+, T6 changed to TC1-1-13M+**
2. **Oscilloscope set to 50R for each port.**
3. **RF (RF0 port) : 1960MHz, -1.95dBm**
   LO (LO IP port) : 1950MHz, -1.25dBm

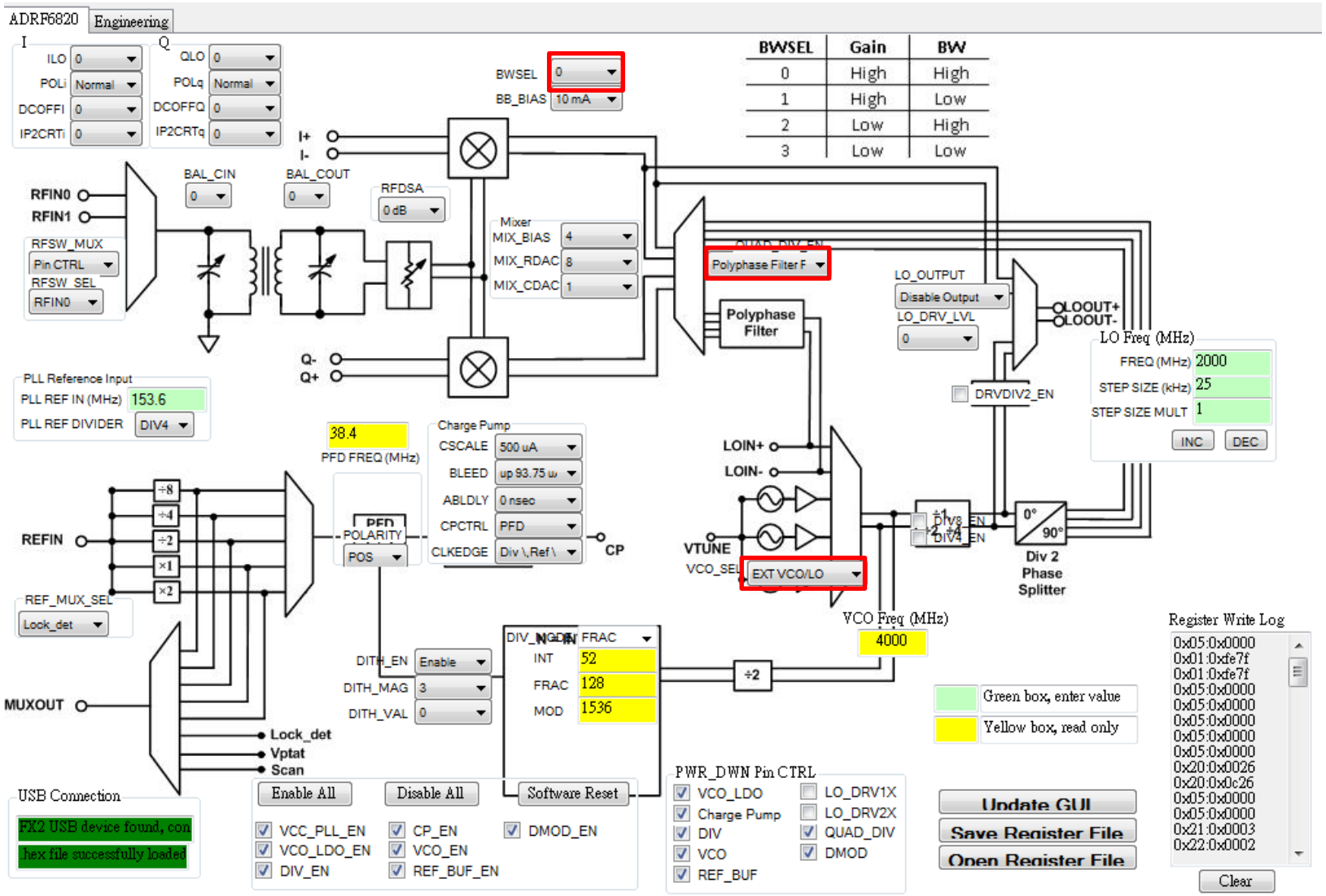




Test 4 (2/3)

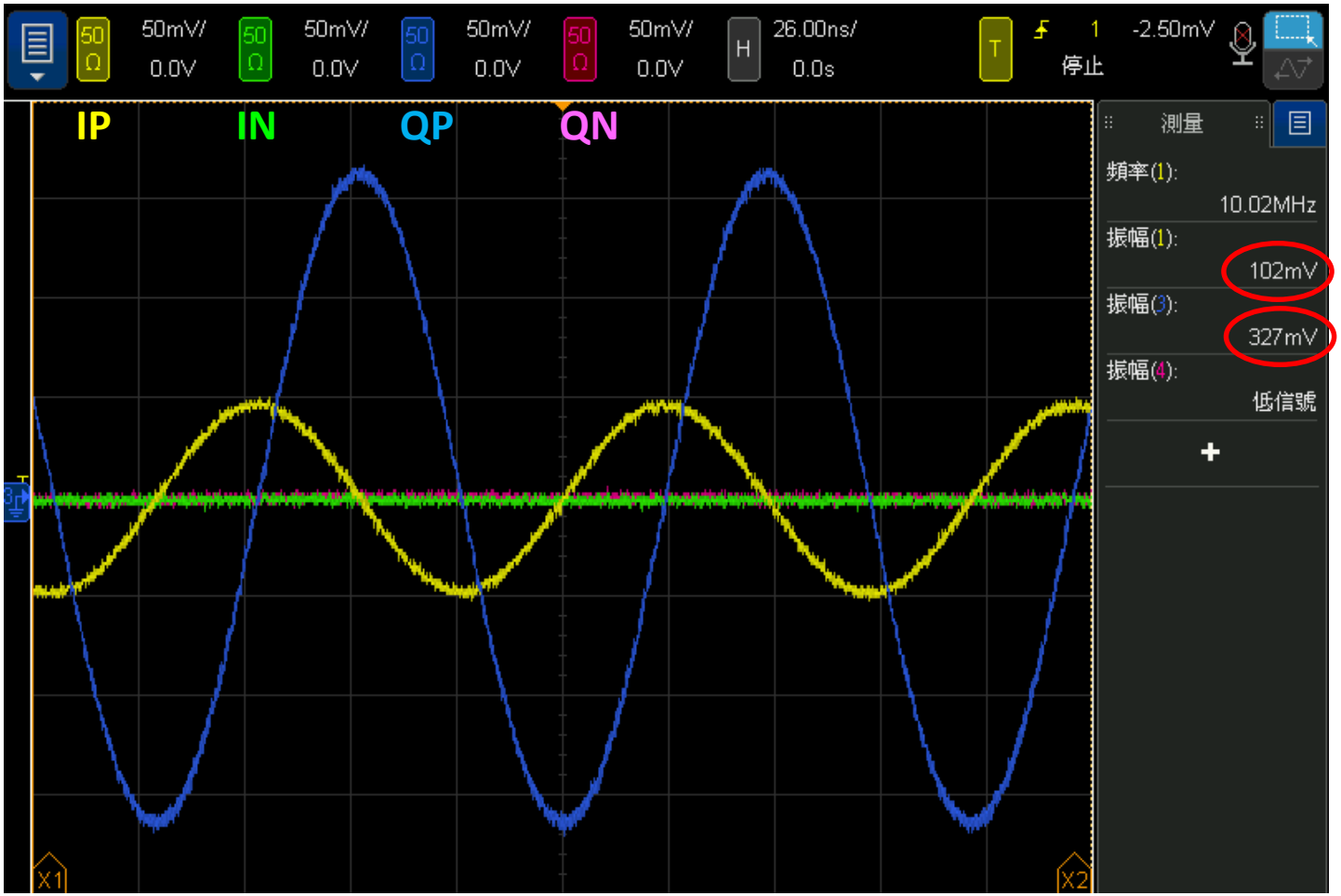
SW Settings :

1. Control SW set to “EXT VCO/LO”, “Polyphase Filter” and “BWSEL=0”.



Test 4 (3/3)

IQ Output



-15.85dBm

-5.73dBm