

<Question1>

Regarding Fig 2, INTV_{CC0} should be connected to PHMODE0 not MODE/CLKIN0, shouldn't it?

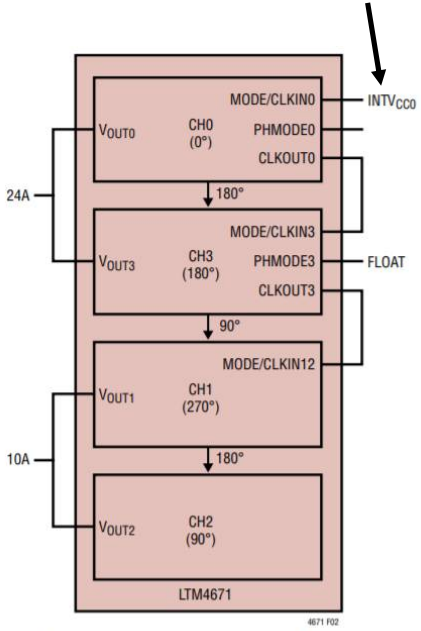


Figure 2. 2 + 2 Parallel Concept Schematic

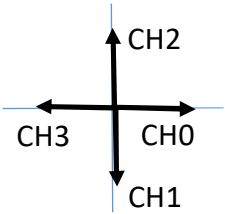
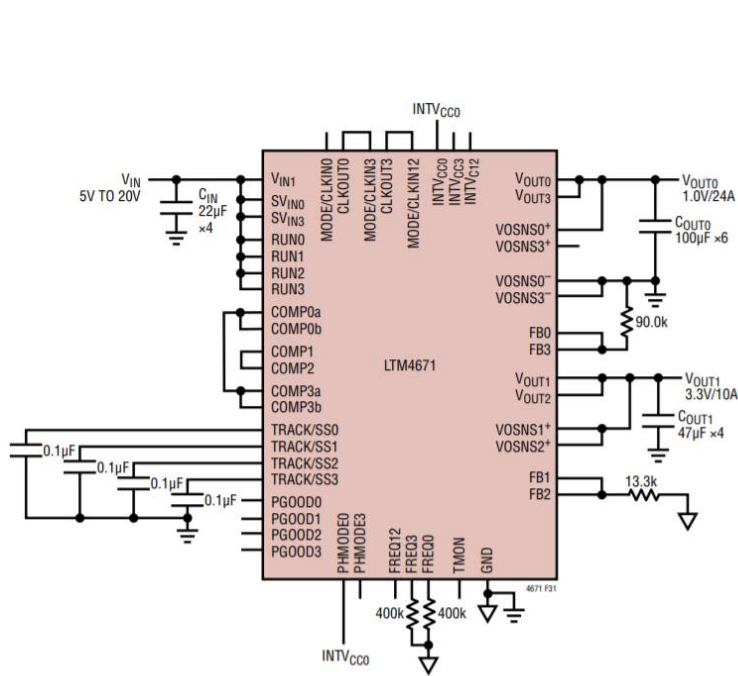


Fig A



	CH0	CH1	CH2	CH3
Phase Shift	180°	90°	180°	180°
Phase	0°	180°	270°	90°

Figure 31. Parallel Operation with 1MHz Clock and Interleaved Phases

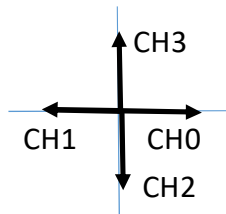


Fig B

<Question2>

Based on the above table, each phase position is set as shown in the Fig B. Is the table correct?

Following connections are the same between the Fig 2 and the Fig 31.
 PHMODE0=INTV_{CC0}, PHMODE3=Float
 CLKOUT0=MODE/CLKIN3, CLKOUT3=MODE/CLKIN12
 So I think the Fig B should be the same as the Fig A.

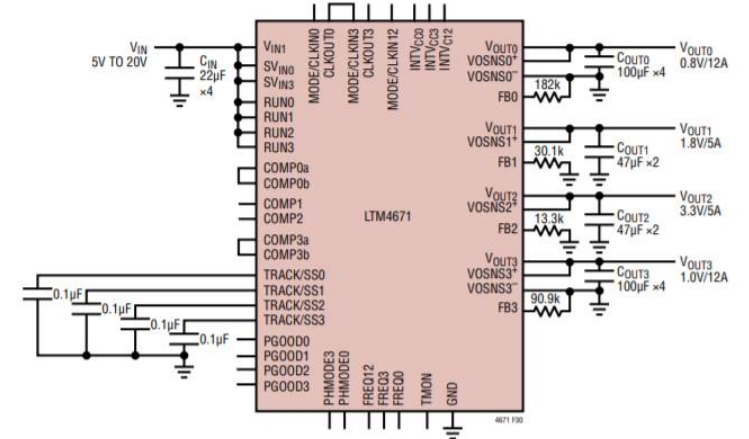
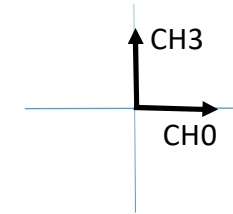


Figure 30. 5V to 20V Input, Quad Output Design

<Question3>

Regarding the Fig 30 case, where is each CH phase position?



Phase difference between CH0 and CH3 of 90 degrees since PHMODE0 is float and CLKOUT0 is connected to MODE/CLKIN3. However I'm not sure CH1 and CH2 positions.