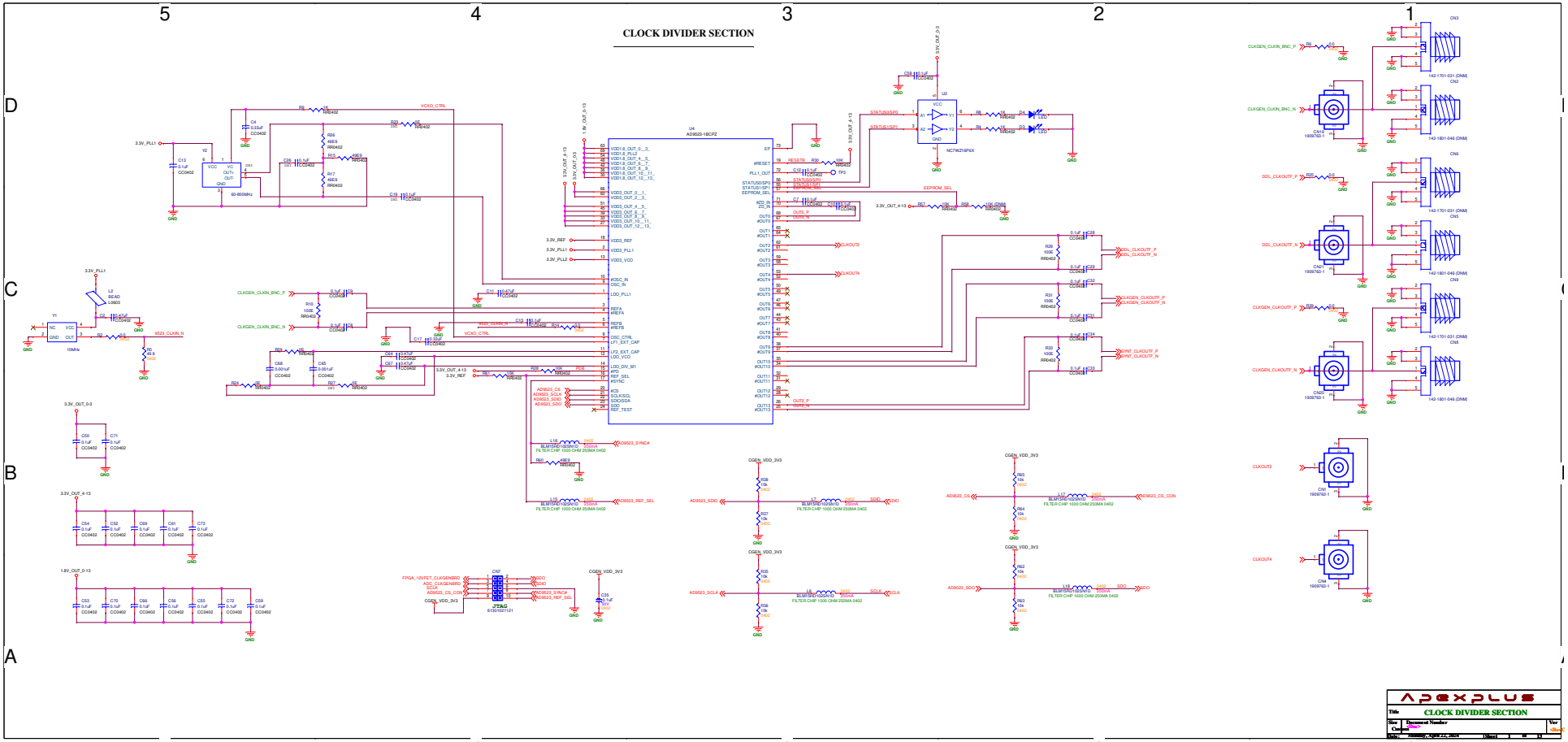
CLOCK DIVIDER SECTION
U4
AD9520-1BCPZ
U2
NC7WZ241P6X
CN3
CN2
CN6
CN9
CN21
CN20
CN7
CN8
CN5
CN4
Y1
10MHz
VCXO_CTRL
CLKGEN_CLKIN_BNC_P
CLKGEN_CLKIN_BNC_N
SDL_CLKOUTP_P
SDL_CLKOUTP_N
CLKGEN_CLKOUTP_P
CLKGEN_CLKOUTP_N
INT_CLKOUTP_P
INT_CLKOUTP_N
CLKOUT2
CLKOUT4
FPGA_12VFET_CLKGENERD
ADC_CLKGENERD
AD9523_CS_CLK
AD9523_SYNC#
AD9523_REF_SEL
JTAG
CGEN_VDD_3V3
AD9523_SCLK
AD9523_SDIO
AD9523_CS
AD9523_SDO
SCLK
SDIO
SDO
CLK
EEPROM_SEL
STATUS0/SPI
STATUS1/SPI
Apexplus
Title
CLOCK DIVIDER SECTION
Size
Document Number
Rev
Custom
Date:
Sheet