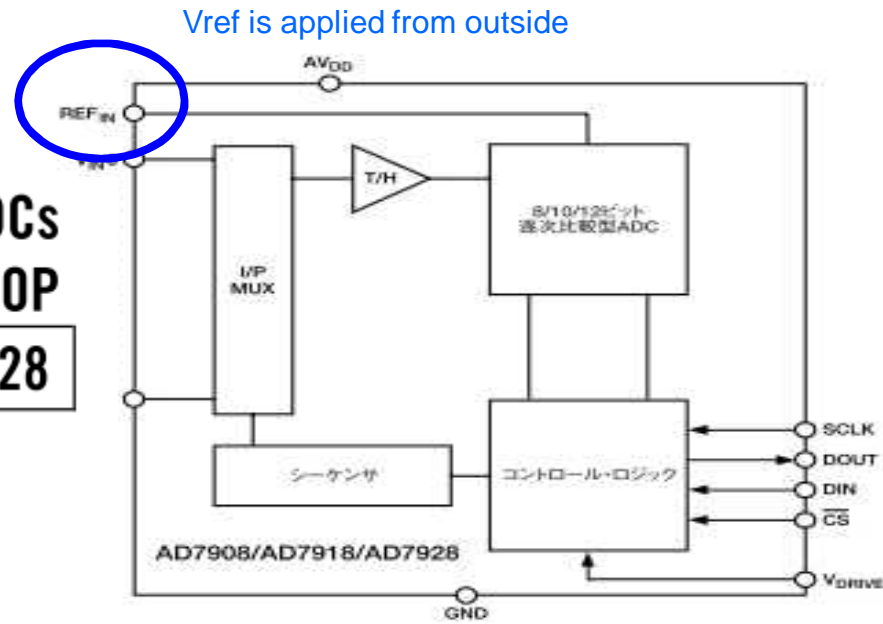


8-Channel, 1 MSPS, 8-/10-/12-Bit ADCs with Sequencer in 20-Lead TSSOP

AD7908/AD7918/AD7928



AD7918 SPECIFICATIONS

$AV_{DD} = V_{DRIVE} = 2.7\text{ V to }5.25\text{ V}$, $REF_{IN} = 2.5\text{ V}$, $f_{SCLK} = 20\text{ MHz}$, $T_A = T_{MIN}$ to T_{MAX} , unless otherwise noted.

Table 2.

Parameter	B Version ¹	Unit	Test Conditions/Comments
DC ACCURACY²			
Resolution	10	Bits	Guaranteed no missed codes to 10 bits Straight binary output coding
Integral Nonlinearity	±0.5	LSB max	
Differential Nonlinearity	±0.5	LSB max	
0 V to REF_{IN} Input Range			
Offset Error	±2	LSB max	
Offset Error Match	±0.2	LSB max	
Gain Error	±0.5	LSB max	
Gain Error Match	±0.2	LSB max	
REFERENCE INPUT			
REF_{IN} Input Voltage	2.5	V	±1% specified performance
DC Leakage Current	±1	µA max	$f_{SAMPLE} = 1\text{ MSPS}$
REF_{IN} Input Impedance	36	kΩ typ	