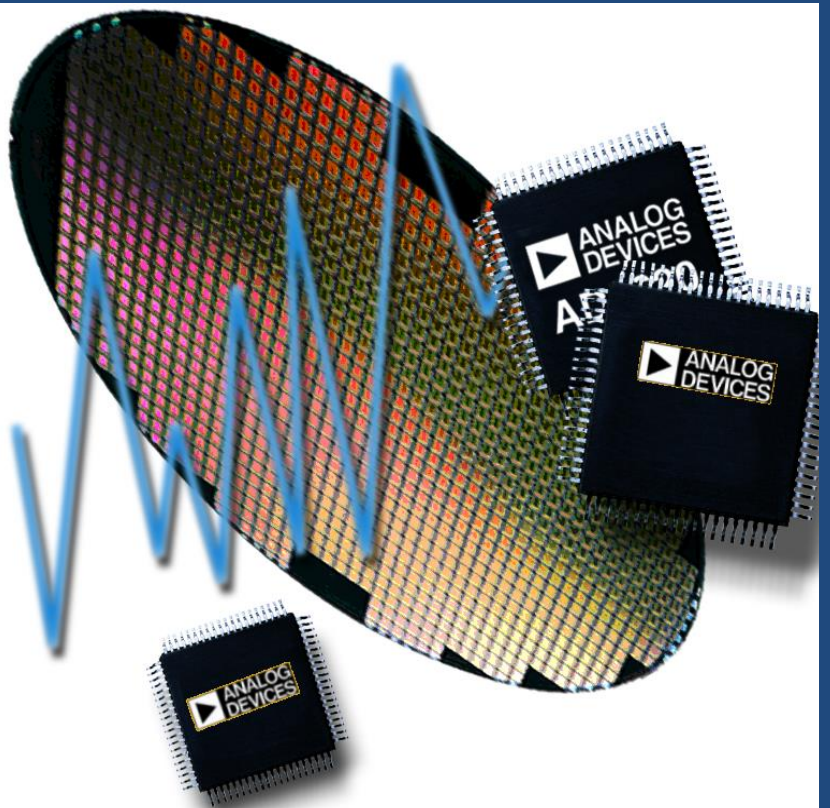


# Analog Devices Welcomes Hittite Microwave Corporation

NO CONTENT ON THE ATTACHED DOCUMENT HAS CHANGED





# ***Reliability Report***

<b>Report Title:</b>	<b>Qualification Test Report</b>
<b>Report Type:</b>	<b>See Attached</b>
<b>Date:</b>	<b>See Attached</b>

# Process FIT Rate Report

**QTR: 2013- 00228**

**Rev: 02**

**Wafer Process: GaAs HBT-A**

HMC247	HMC433	HMC515	HMC735
HMC358	HMC434	HMC529	HMC736
HMC361	HMC437	HMC530	HMC737
HMC362	HMC438	HMC531	HMC738
HMC363	HMC439	HMC532	HMC739
HMC364	HMC440	HMC533	HMC764
HMC365	HMC443	HMC534	HMC765
HMC369	HMC444	HMC535	HMC783
HMC370	HMC445	HMC582	HMC807
HMC384	HMC447	HMC583	
HMC385	HMC466	HMC584	
HMC386	HMC492	HMC586	
HMC388	HMC493	HMC587	
HMC389	HMC494	HMC588	
HMC390	HMC500	HMC606	
HMC391	HMC505	HMC630	
HMC394	HMC506	HMC631	
HMC398	HMC507	HMC632	
HMC401	HMC508	HMC695	
HMC403	HMC509	HMC698	
HMC416	HMC510	HMC699	
HMC429	HMC511	HMC705	
HMC430	HMC512	HMC732	
HMC431	HMC513	HMC733	
HMC432	HMC514	HMC734	

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## Introduction

The testing performed for this report is designed to accelerate the predominant failure mode, electro-migration (EM), for the devices under test. The devices are stressed at high temperature and DC biased to simulate a lifetime of use at typical operating temperatures. Using the Arrhenius equation, the acceleration factor (AF) is calculated for the stress testing based on the stress temperature and the typical use operating temperature.

This report is intended to summarize all of the High Temperature Operating Life Test (HTOL) data for the GaAs HBT-A process. The FIT/MTTF data contained in this report includes all the stress testing performed on this process to date and will be updated periodically as additional data becomes available. Data sheets for the tested devices can be found at [www.hittite.com](http://www.hittite.com).

## Glossary of Terms & Definitions:

1. **CDM:** Charged Device Model. A specified ESD testing circuit characterizing an event that occurs when a device acquires charge through some triboelectric (frictional) or electrostatic induction processes and then abruptly touches a grounded object or surface. This test was performed in accordance with JEDEC 22-C101.
2. **ESD:** Electro-Static Discharge. A sudden transfer of electrostatic charge between bodies or surfaces at different electrostatic potentials.
3. **HBM:** Human Body Model. A specified ESD testing circuit characterizing an event that occurs when a device is subjected to an electro-static charge stored in the human body and discharged through handling of the electronic device. This test was performed in accordance with JEDEC 22-A114.
4. **HTOL:** High Temperature Operating Life. This test is used to determine the effects of bias conditions and temperature on semiconductor devices over time. It simulates the devices' operating condition in an accelerated way, through high temperature and/or bias voltage, and is primarily for device qualification and reliability monitoring. This test was performed in accordance with JEDEC JESD22-A108.
5. **HTSL:** High Temperature Storage Life. Devices are subjected to 1000 hours at 150°C per JESD22-A103.
6. **MSL:** Moisture sensitivity level pre-conditioning is performed per JESD22-A113.
7. **Operating Junction Temp ( $T_{oj}$ ):** Temperature of the die active circuitry during typical operation.
8. **Stress Junction Temp ( $T_{sj}$ ):** Temperature of the die active circuitry during stress testing.
9. **UHAST:** Unbiased Highly Accelerated Stress Test. Devices are subjected to 96 hours of 85% relative humidity at a temperature of 130°C and pressure per JESD22-A118.

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## Qualification Sample Selection:

All qualification devices used were manufactured and tested on standard production processes and met pre-stress acceptance test requirements.

## Summary of Qualification Tests:

### HMC365 Qualification (QTR2004-00001)

TEST	QTY IN	QTY OUT	PASS/FAIL	NOTES
Initial Electrical	12	12	Complete	
HTOL, 1240 hours	12	12	Complete	
Post Electrical Test	12	12	Pass	

### HMC510 Qualification (QTR2011-00004)

TEST	QTY IN	QTY OUT	PASS/FAIL	NOTES
Initial Electrical	12	12	Complete	
HTOL, 1000 hours	12	12	Complete	
Post Electrical Test	12	12	Pass	

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**HMC511 Qualification (QTR2011-00013)**

TEST	QTY IN	QTY OUT	PASS/FAIL	NOTES
Initial electrical Test	554	554	Pass	
MSL-3	554	554	Complete	Devices passed post MSL 3 C-SAM.
Final Electrical Test	554	554	Pass	
UHASt	80	80	Complete	80 units from NC94
Final electrical Test	80	80	Pass	
Temp. Cycle	160	160	Complete	80 units from lot MMCR 80 units from lot NC94
Final electrical Test	160	160	Pass	
HTSL	137	137	Complete	61 units from lot MMCR 76 units from lot NC94
Final Electrical Test	137	137	Pass	
HTOL	159	159	Complete	80 units from lot MMCR 79 units from lot NC94
Final Electrical test	159	159	Pass	
Physical Dimensions	22	22	Pass	All dimensions passed per Hittite's datasheet outline drawing.

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TEST	QTY IN	QTY OUT	PASS/FAIL	NOTES
Solderability	4	4	Pass	Note: Data from previously tested units from a different lot. Two units were tested to JESD22-B102E Method 1 and 2 units to Method 2.
ESD Exposure	3 per Voltage Level	3 per Voltage Level	Complete	Devices tested from lot NC94
Electrical Test	3 per Voltage Level	3 per Voltage Level	Class II	CDM passed 250 Volts

## HMC416 QTR2012-00198

TEST	QTY IN	QTY OUT	PASS/FAIL	NOTES
Initial electrical Test	24	24	Pass	
HTOL, 1000 hours	24	24	Complete	
Final Electrical test	24	24	Pass	

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**HMC361 QTR2012-00255**

TEST	QTY IN	QTY OUT	PASS/FAIL	NOTES
Initial electrical Test	104	104	Pass	
Burn-in, 240 hours	80	80	Complete	
Final Electrical test	80	80	Pass	
HTOL, 1000 hours	24	24	Complete	
Final Electrical test	24	24	Pass	

**HMC6XXX (QTR2013-00340)**

TEST	QTY IN	QTY OUT	PASS / FAIL	NOTES
Initial Electrical	6	6	Complete	
HTOL, 5039 hours	6	6	Complete	
Post HTOL Electrical Test	6	6	Pass	

**HMC6XXX (QTR2013-00340)**

TEST	QTY IN	QTY OUT	PASS / FAIL	NOTES
Initial Electrical	14	14	Complete	
HTOL, 2000 hours	14	14	Complete	
Post HTOL Electrical Test	14	14	Pass	

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## GaAs HBT-A Failure Rate Estimate

Based on the HTOL test results, a failure rate estimation was determined using the following parameters:

With device ambient case temp,  $T_c = 65^\circ\text{C}$

HMC365 (QTR2004-00001)

Operating Junction Temp ( $T_{oj}$ ) =  $65^\circ\text{C}$  ( $338^\circ\text{K}$ )

Stress Junction Temp ( $T_{sj}$ ) =  $125^\circ\text{C}$  ( $398^\circ\text{K}$ )

HMC510 (QTR2011-00004)

Operating Junction Temp ( $T_{oj}$ ) =  $65^\circ\text{C}$  ( $338^\circ\text{K}$ )

Stress Junction Temp ( $T_{sj}$ ) =  $125^\circ\text{C}$  ( $398^\circ\text{K}$ )

HMC511 (QTR2011-00013)

Operating Junction Temp ( $T_{oj}$ ) =  $107^\circ\text{C}$  ( $380^\circ\text{K}$ )

Stress Junction Temp ( $T_{sj}$ ) =  $135^\circ\text{C}$  ( $408^\circ\text{K}$ )

HMC416 (QTR2012-00198)

Operating Junction Temp ( $T_{oj}$ ) =  $119^\circ\text{C}$  ( $392^\circ\text{K}$ )

Stress Junction Temp ( $T_{sj}$ ) =  $175^\circ\text{C}$  ( $448^\circ\text{K}$ )

HMC361 (QTR2012-00255) (240 hour test)

Operating Junction Temp ( $T_{oj}$ ) =  $105^\circ\text{C}$  ( $378^\circ\text{K}$ )

Stress Junction Temp ( $T_{sj}$ ) =  $165^\circ\text{C}$  ( $438^\circ\text{K}$ )

HMC6XXX (QTR2013-00340) (5039 hour test)

Operating Junction Temp ( $T_{oj}$ ) =  $111^\circ\text{C}$  ( $384^\circ\text{K}$ )

Stress Junction Temp ( $T_{sj}$ ) =  $143^\circ\text{C}$  ( $416^\circ\text{K}$ )

HMC6XXX (QTR2013-00340) (2000 hour test)

Operating Junction Temp ( $T_{oj}$ ) =  $111^\circ\text{C}$  ( $384^\circ\text{K}$ )

Stress Junction Temp ( $T_{sj}$ ) =  $143^\circ\text{C}$  ( $416^\circ\text{K}$ )

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Device hours:

- HMC365 (QTR2004-00001) = (12 X 1240hrs) = 14,880 hours
- HMC510 (QTR2011-00004) = (12 X 1000hrs) = 12,000 hours
- HMC511 (QTR2011-00013) = (159 X 1000hrs) = 159,000 hours
- HMC416 (QTR2012-00198) = (24 X 1000hrs) = 24,000 hours
- HMC361 (QTR2012-00255, 240 hour test) = (80 X 240hrs) = 19,200 hours
- HMC361 (QTR2012-00255, 1000 hour test) = (12 X 1000hrs) = 12,000 hours
- HMC6XXX (QTR2013-00340, 5039 hour test) = (6 X 5039hrs) = 30,234 hours
- HMC6XXX (QTR2013-00340, 2000 hour test) = (14 X 2000hrs) = 28,000 hours

For GaAs HBT-A MMIC, Activation Energy = 1.1 eV

$$AF = \exp\left[\left(\frac{E_A}{k}\right) \cdot \left(\left(\frac{1}{T_{USE}}\right) - \left(\frac{1}{T_{STRESS}}\right)\right)\right]$$

Acceleration Factor (AF):

- HMC365 (QTR2004-00001) Acceleration Factor =  $\exp[1.1/8.6 \times 10^{-5}(1/338-1/398)] = 300.3$
- HMC510 (QTR2011-00004) Acceleration Factor =  $\exp[1.1/8.6 \times 10^{-5}(1/338-1/398)] = 300.3$
- HMC511 (QTR2011-00013) Acceleration Factor =  $\exp[1.1/8.6 \times 10^{-5}(1/380-1/408)] = 10.1$
- HMC416 (QTR2012-00198) Acceleration Factor =  $\exp[1.1/8.6 \times 10^{-5}(1/392-1/448)] = 59.1$
- HMC361 (QTR2012-00255, 240 hour test) Acceleration Factor =  $\exp[1.1/8.6 \times 10^{-5}(1/378-1/438)] = 103.1$
- HMC361 (QTR2012-00255, 1000 hour test) Acceleration Factor =  $\exp[1.1/8.6 \times 10^{-5}(1/378-1/438)] = 103.1$
- HMC6XXX (QTR2013-00340, 5039 hour test) Acceleration Factor =  $\exp[1.1/8.6 \times 10^{-5}(1/384-1/416)] = 45.7$
- HMC6XXX (QTR2013-00340, 2000 hour test) Acceleration Factor =  $\exp[1.1/8.6 \times 10^{-5}(1/384-1/416)] = 45.7$

Equivalent hours = Device hours x Acceleration Factor

Equivalent hours =

$$(14,880 \times 300.3) + (12,000 \times 300.3) + (159,000 \times 10.1) + (24,000 \times 59.1) + (19,200 \times 103.1) + (12,000 \times 103.1) + (30,324 \times 45.7) + (28,000 \times 45.7) = 1.70 \times 10^7 \text{ hours}$$

Since there were no failures and we used a time terminated test, F=0, and R = 2F+2 = 2

The failure rate was calculated using Chi Square Statistic:

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$$\lambda_{CL} = \frac{\chi^2_{\%CL, 2f+2} \cdot 10^9}{2 \cdot t \cdot SS \cdot AF}$$
 at 60% and 90% Confidence Level (CL), with 0 units out of spec and a 65°C package backside temp;

## Failure Rate

$\lambda_{60} = [(\chi^2)_{60,2}]/(2X \cdot 1.70 \times 10^7) = 1.8 / 3.39 \times 10^7 = 5.39 \times 10^{-8}$  failures/hour or 53.9 FIT or MTTF =  $1.85 \times 10^7$  Hours

$\lambda_{90} = [(\chi^2)_{90,2}]/(2X \cdot 1.70 \times 10^7) = 4.6 / 3.39 \times 10^7 = 1.36 \times 10^{-7}$  failures/hour or 135.8 FIT or MTTF =  $7.36 \times 10^6$  Hours

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