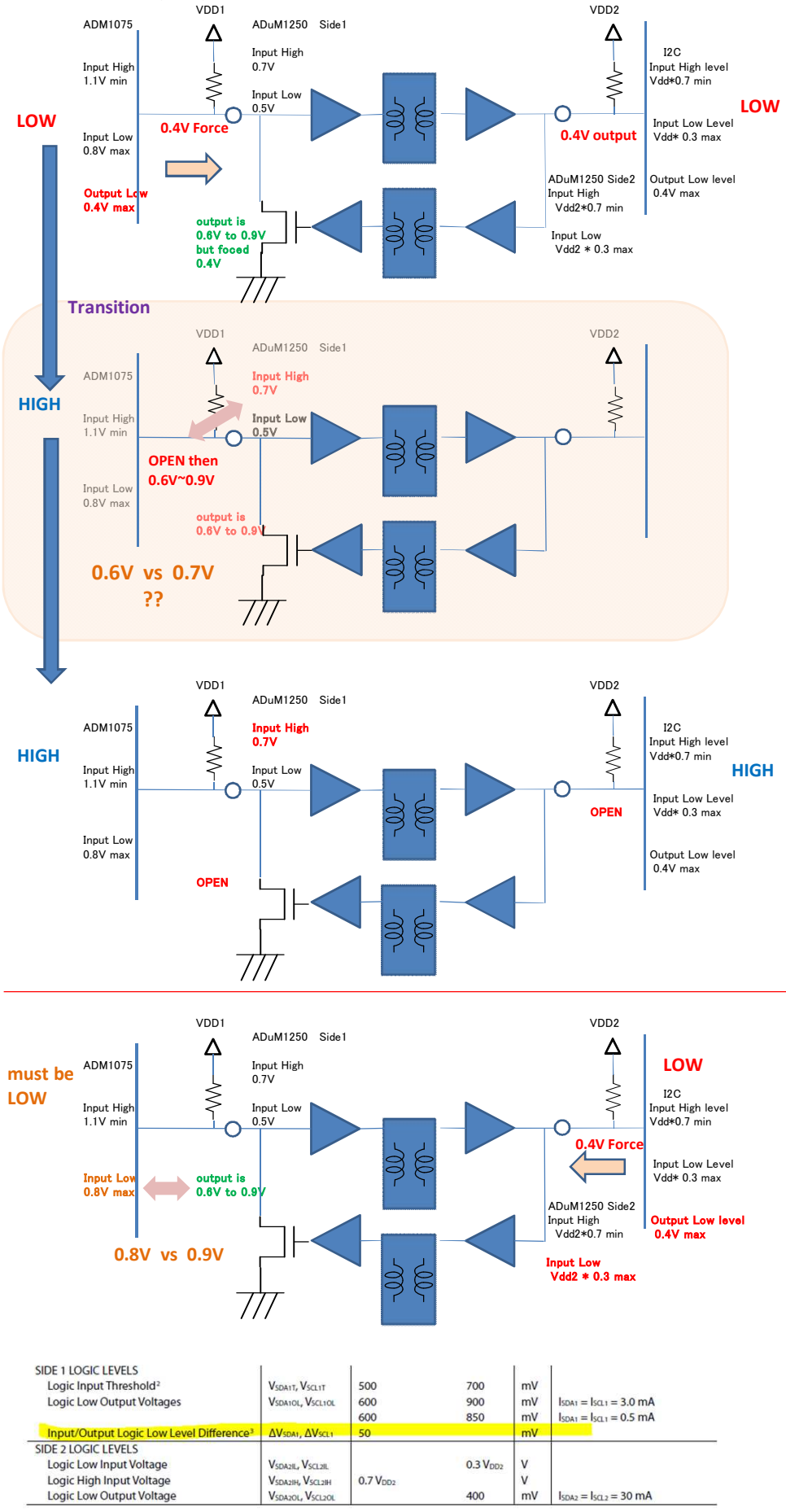
ADM1075 and ADuM1250 connection



SIDE 1 LOGIC LEVELS					
Logic Input Threshold[2]	V_{SDA1T}, V_{SCL1T}	500	700	mV	
Logic Low Output Voltages	V_{SDA1OL}, V_{SCL1OL}	600	900	mV	$I_{SDA1} = I_{SCL1} = 3.0$ mA
		600	850	mV	$I_{SDA1} = I_{SCL1} = 0.5$ mA
Input/Output Logic Low Level Difference[3]	ΔV_{SDA1}, ΔV_{SCL1}	50		mV	
SIDE 2 LOGIC LEVELS					
Logic Low Input Voltage	V_{SDA2IL}, V_{SCL2IL}		0.3 V_{DD2}	V	
Logic High Input Voltage	V_{SDA2IH}, V_{SCL2IH}	0.7 V_{DD2}		V	
Logic Low Output Voltage	V_{SDA2OL}, V_{SCL2OL}		400	mV	$I_{SDA2} = I_{SCL2} = 30$ mA