

A New Polyphase Mixed-Mode Bandpass Filter Section Using Current-Feedback Operational Amplifiers

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Abstract—A new polyphase mixed-mode filter using commercially available integrated circuits is presented. The circuit uses two current-feedback operational amplifiers, four grounded resistors and two grounded capacitors. The proposed circuit enjoys independent grounded-resistance control of its parameters. Simulation results using are included.

I. INTRODUCTION

Polyphase filters [1], also known as complex analog filters [2], vector filters [3] and sequence discriminators [4], are widely used for generation of quadrature signals and image rejection in the analog front-end of radio frequency integrated wireless transceivers [5]-[8]. They can also be used for generation and detection of single sideband signals [9]-[11] and frequency division multiplex-communication systems [4].

Passive polyphase filters, using only resistors and capacitors, are widely used; see for example [5],[6] and [12]-[14]. However, cascading of identical passive polyphase filters, in order to obtain higher-order filters, results in loading effects and substantially complicates the synthesis process [7]. While it is possible to obtain analytical expressions for second-, third and probably higher-order filters [15], these expressions are very complicated and complicate the design of higher-order passive polyphase filters. Alternatively, additional buffers must be inserted among stages to overcome these effects [14]. Active polyphase filters have, therefore, emerged using operational amplifiers[16]-[18],operational transconductance amplifiers [19]-[22], current mirrors [23], second-generation current-conveyors [24] and current-feedback operational amplifiers [25].

While the selection of an appropriate implementation technique for the active polyphase filters depends on the specifications imposed by the intended application [26], some general observations can be made. Operational amplifiers have finite values for the gain-bandwidth product. This will limit the signal frequencies [23] and will result in errors in the polyphase filter transfer function [17] and [27]. Operational transconductance amplifier based realizations either require a large number of transconductance elements [19]-[22] or suffer from the excessive dispersion in the values of the passive components [18] and/or transistor mismatches [16]. Current-mirror based realizations suffer from the parasitic capacitances [23] and the current-mirror errors. The performances of current-conveyor based circuits, in terms of bandwidth, linearity and dynamic range, are better than the operational amplifier and the operational transconductance based circuits. Moreover, errors in the transfer functions of current-conveyor based circuits, resulting from the conveyor nonidealities, can be easily compensated than those resulting from amplifier

nonidealities in operational amplifier based circuits [24]. Current-feedback operational amplifiers are no more than a plus-type second-generation current conveyor plus a voltage buffer. Therefore, while current-feedback operational amplifier based realizations are expected to enjoy the same attractive advantages of the current-conveyor based realizations, they have the additional advantage of providing a low impedance output voltage. This makes easy the cascading of similar filter sections to achieve higher-order filters.

Despite the expected advantages in using current-conveyors or current-feedback operational amplifiers for designing polyphase filters, only two realizations are reported [24] and [25]. The current-conveyor based realization reported in [24] is a current-mode realization with current-input and current-output. It uses three plus-type and one minus-type second-generation current conveyors. A minus-type current conveyor is not commercially available and can be realized using two of the commercially available plus-type second-generation current-conveyors. Therefore the practical implementation of the circuit proposed in [24] requires five plus-type second-generation current-conveyors. The current-feedback operational amplifier based realization reported in [25] is a voltage-mode realization with voltage-input and voltage-output. It uses three current-feedback operational amplifiers and requires four floating capacitors. Obviously, this will limit its signal frequency operation.

The major intention of this paper is, therefore, to present a new current-feedback operational amplifier based realization for a first-order polyphase filter. The proposed circuit is a mixed-mode with current-input and voltage-output. It uses two grounded capacitors and four grounded resistors and can be easily converted into voltage-mode or current mode by adding two additional second-generation current-conveyors. The proposed circuit also enjoys a low output impedance node and a low input impedance node. Thus, it can be easily cascaded to obtain higher-order filters.

II. PROPOSED CIRCUIT

The proposed circuit is shown in Fig. 1. Assuming ideal current-feedback operational amplifiers with $i_y = 0, i_z = \pm i_x, v_y = v_x$ and $v_w = v_z$, routine analysis yields the following transfer functions

$$\frac{v_{o1}}{i_1} = \frac{1}{G_1 + j(\omega C_1 - G_{m1})} \quad (1)$$

$$\frac{v_{o2}}{i_2} = \frac{1}{G_2 + j(\omega C_2 - G_{m2})} \quad (2)$$

In deriving equations (1) and (2) it is assumed that the output voltages are in quadrature that is $v_{o1} = jv_{o2}$ [5] and [18].

With $G_1 = G_2 = 1/R, C_1 = C_2 = C$ and $G_{m1} = G_{m2} = 1/R_m$, equations (1) and (2) can be rewritten as

$$\frac{v_{o1(2)}}{i_{1(2)}} = \frac{R}{1 + j\left(\frac{\omega - \omega_c}{\omega_o}\right)} \quad (3)$$

$$\text{where } \omega_c = \frac{1}{R_m C} \text{ and } \omega_o = \frac{1}{RC}.$$

Equation (3) is the transfer function of a mixed-mode bandpass filter with a symmetrical transfer centered around ω_c and asymmetrical transfer function around the zero frequency. Equation (3), therefore, represents the transfer function of a complex analog bandpass filter that can be used for image rejection and sequence discrimination [2].

III. NONIDEAL ANALYSIS

Current-feedback operational amplifiers are nonideal devices suffering from current- and voltage-tracking errors. Therefore, the effect of the amplifier nonidealities on the performance of the proposed filter must be studied. Assuming that the amplifiers are identical with nonideal characteristics expressed by $i_z = \pm \alpha i_x, v_y = \beta v_x$ and $v_w = \gamma v_z$, where $\alpha = 1 - \varepsilon_i, |\varepsilon_i| \ll 1$ represents the current-tracking error, $\beta = 1 - \varepsilon_v, |\varepsilon_v| \ll 1$ represents the input voltage tracking error and $\gamma = 1 - \sigma_v, |\sigma_v| \ll 1$ represents the output voltage tracking errors, reanalysis yields the following transfer function

$$\frac{v_{o1(2)}}{i_{1(2)}} = \frac{\beta \gamma R}{1 + j \left[\frac{\omega - \frac{\beta}{\alpha} \omega_c}{\omega_o} \right]} \quad (4)$$

Comparison between equations (3) and (4) clearly shows that the effect of the current-feedback operational nonidealities can be easily compensated.

Current-feedback operational amplifiers also suffer from parasitic resistances and capacitances at terminals, x , y and z with r_x around few tens of ohms, r_y and r_z of the order of few mega ohms, and C_y and C_z of the order of few pico-farads. With R_{m1} and R_{m2} in parallel with the parasitic resistances and capacitances at terminals y and z , then proper selection of the values of these resistances can eliminate the effect of the parasitic components. To study the effect of the resistance r_x , the circuit of Fig. 1 was reanalyzed assuming $v_x = v_y + i_x r_x$ and identical current-feedback operational amplifiers. Equation (3) becomes

$$\frac{v_{o1(2)}}{i_{1(2)}} = \frac{\frac{R}{1 + \frac{\omega r_x CR}{R_m}}}{1 + j\left(\frac{\omega - \omega_{cn}}{\omega_{on}}\right)} \quad (5)$$

where $\omega_{cn} = \frac{R + r_x}{R_m RC}$ and $\omega_{on} = \frac{1 + \frac{\omega r_x CR}{R_m}}{RC}$. From equation (5), it appears that a proper selection of the components so that $\omega r_x CR / R_m \ll 1$ and $r_x \ll R$, then $\omega_{on} = \omega_n$ and $\omega_{cn} = \omega_c$. Thus, the effect of the finite value of the resistance r_x can be eliminated.

IV. SIMULATION RESULTS

To confirm the operability of the proposed circuit of Fig. 1, the circuit was simulated as an image filter using HSPICE and its built-in model of the current-feedback operational amplifier AD844. The results obtained with $R_1 = R_2 = 1590\Omega, R_{m1} = R_{m2} = 159\Omega$ and $C_1 = C_2 = 10nF$ are shown in Fig. 2. From Fig. 2 it appears that an image rejection of 20 dB is feasible using the proposed circuit of Fig. 1.

V. CONCLUSION

In this paper, a new polyphase mixed-mode, with input current and output voltage, bandpass filter has been presented. The circuit uses two current-feedback operational amplifiers, four grounded resistors and two grounded capacitors. Using a simple voltage-to-current converter at its output, it can be easily cascaded to obtain higher-order mixed-mode filters. Furthermore, the proposed filter enjoys low sensitivity to parasitic, independent grounded-resistance control for its

bandwidth and center frequency and uses off-the-shelf components.

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REFERENCES

- [1] M.J. Gingell, A symmetrical polyphase network, British Patent, 1,174,710, 17 December 1969
- [2] A.S. Sedra, W.M. Snelgrove and R. Allen, Complex analog bandpass filters designed by linearly shifting real low-pass prototypes, Proceedings IEEE International Symposium on Circuits and Systems, 1985, pp.1223-1226
- [3] T. Hornak, K.L. Knudsen, A.Z. Grzegorek, K.A. Nishimura and W.J. McFarland, An image-rejecting mixer and vector filter with 55-dB image rejection over process, temperature, and transistor mismatch, IEEE Journal of Solid-State Circuits, Vol. 36, 2001, pp.23-33
- [4] W.B. Mikhael, Sequence discriminators and their use in frequency division multiplex-communication systems, IEEE Transactions on Circuits and Systems, Vol. CAS-26, 1979, pp.117-129
- [5] A. Rofougaran, G. Chang, J.J. Rael, J.Y.-C. Chang, M. Rofougaran, P.J. Chang, M. Djafari, M.-K. Ku, E.W. Roth, A.A. Abidi and H. Samueli, A single-chip 900 MHz spread-spectrum wireless transceiver in 1- μ m CMOS-Part I: Architecture and transmitter design, IEEE Journal of Solid-State Circuits, Vol. 33, 1998, pp.515-534
- [6] F. Behbahani, Y. Kishigami, J. Leete and A.A. Abidi, CMOS mixers and polyphase filters for large image rejection, IEEE Journal of Solid-State Circuits, Vol. 36, 2001, pp.873-887
- [7] B.J. Minnis and P.A. Moore, Non-complex signal processing in a low-IF receiver, IEE Proceedings-Circuits Devices Systems, Vol. 149, 2002, pp.322-330
- [8] J. Crols and M. Steyaert, CMOS Wireless Transceiver Design, Kluwer Academic Publishers, 1997
- [9] M.J. Gingell, Single sideband modulation using sequence asymmetric polyphase networks, Electrical Communication, Vol.48, 1973, pp. 21-25
- [10] E. Daoud, W.B. Mikhael and M.N.S. Swamy, New active RC-networks for the generation and detection of single-sideband signals, IEEE Transactions on Circuits and Systems, Vol. CAS-27, 1980, pp.1140-1154
- [11] K. Martin, Complex signal processing is not-complex, Proceedings of the European Solid State Circuits Conference, 2003, pp.3-14
- [12] J. Crols and M.S.J. Steyaert, A single-chip 900 MHz CMOS receiver front-end with a high performance low-IF topology, IEEE Journal of Solid-State Circuits, Vol. 30, 1995, pp.1483-1492
- [13] S.H. Galal, H.F. Ragaie and M.S. Tawfik, RC sequence asymmetric polyphase networks for RF integrated transceivers, IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 47, 2000, pp. 18-27
- [14] F. Behbahani, J.C. Leete, Y. Kishigami, A. Roithmeier, K. Hoshino and A.A. Abidi, A 2.4-GHz low-IF receiver for wideband WLAN in 0.6- μ m CMOS-Architecture and front-end, IEEE Journal of Solid-State Circuits, Vol. 35, 2000, pp.1908-1916
- [15] H. Kobayashi, T. Kitahara, S. Takigami and H. Sadamura, Explicit transfer function of RC polyphase filter for wireless transceiver analog front-end, Proceedings IEEE Asia Pacific Conference on ASIC, 2002, pp. 137-140
- [16] J. Crols and M.S.J. Steyaert, An analog integrated polyphase filter for a high performance low-IF receiver, Symposium on VLSI Circuits Digest of Technical Papers, 1995, Kyoto, Japan, pp.87-88
- [17] E. Stikvoort, Polyphase filter section with OPAMPs, IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 50, 2003, pp. 376-378
- [18] T. Hornak, Using polyphase filters as image attenuators, RF Design, June 2001, pp.26-34
- [19] P. Andreani, S. Mattisson and B. Essink, A CMOS g_m -C polyphase filter with high image band rejection, Proceedings of the European Solid State Circuits Conference, 2000, pp.244-247
- [20] C. Muto, A new extended frequency transformation for complex analog filter design, IEICE Transactions on Fundamentals, Vol. E83-A, 2000, pp.934-940
- [21] B. Shi, W. Shan and P. Andreani, A 57-dB image band rejection CMOS g_m -C polyphase filter with automatic frequency tuning for Bluetooth, IEEE International Symposium on Circuits and Systems, 2002, pp. V-169-V-172
- [22] T. Melly, E.L. Roux, D. Ruffieux and V. Peiris, A 0.6mA, 0.9V 100 MHz FM front-end in a 0.18 μ m CMOS-D technology, Proceedings of the European Solid State Circuits Conference, 2003, pp.417-420
- [23] C.Y. Chou and C.Y. Wu, The design of a wideband and low-power CMOS active polyphase filter for low-IF receiver applications, Proceedings of the IEEE Asia Pacific Conference on Circuits and Systems, 2002, pp.241-244
- [24] X. Zhang, N. Kambayashi and Y. Shinada, A realization of active current-mode resonator with complex coefficients using CCII, IEICE Transactions on Fundamentals, Vol. E80-A, 1997, pp. 413-415
- [25] M. Un and S. Cekil, Polyphase filter realization with CFAS, Third International Conference on Earth Sciences and Electronics, Istanbul, Turkey, 2003, pp. 1-14
- [26] J. Crols and M. Steyaert, Low-IF topologies for high performance analog front ends of fully integrated receivers, IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, 1998, pp. 269-282
- [27] T. Hornak, Gain and frequency stabilization in polyphase filters with low open-loop gain operational amplifiers, RF Design, August 2003, pp.44-50,78

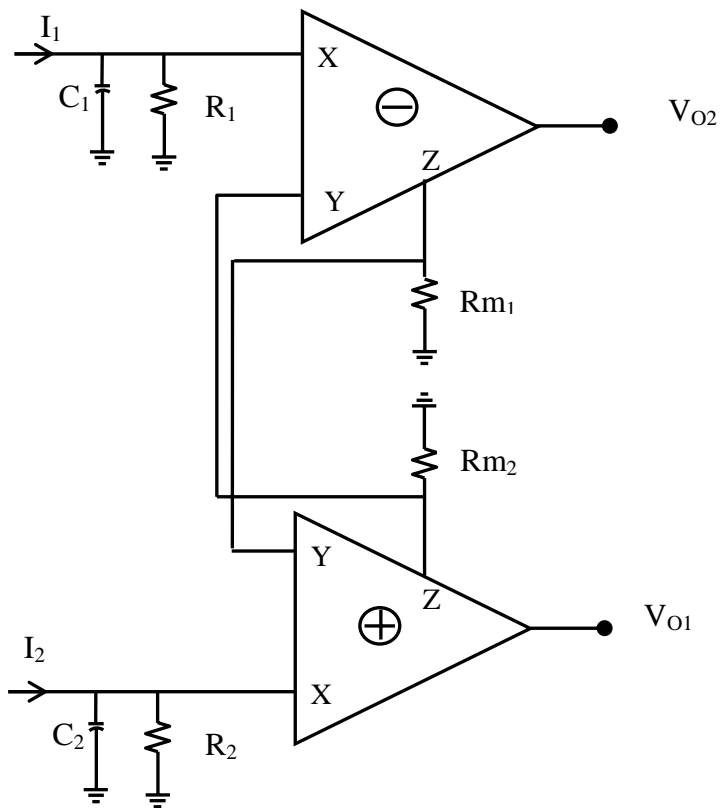


Figure1: Proposed Polyphase Mixed-Mode Bandpass Filter

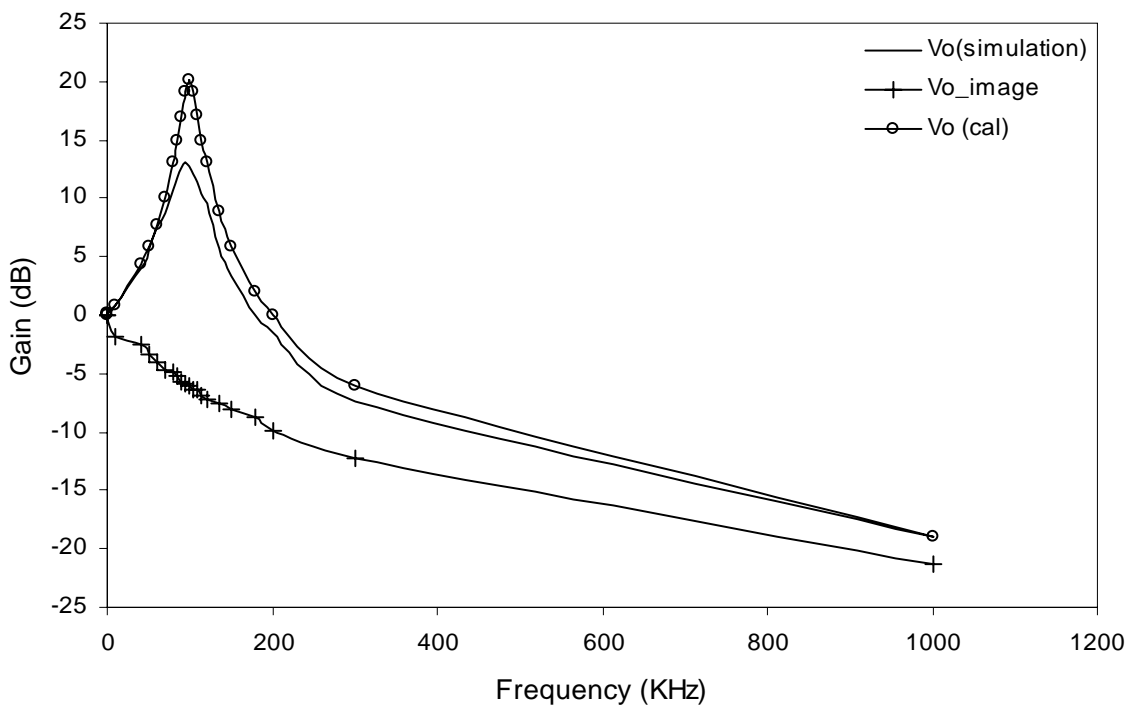


Fig. 2 Theoretical and Simulated Response of the Proposed Polyphase Mixed-Mode Bandpass Filter