

INTRODUCTION

This document describes ADI register setting recommendations for the ADV7174/ADV7179. This document must be used in conjunction with the latest ADV7174/ADV7179 data sheet.

ADV7174/ADV7179 RECOMMENDED SETTINGS

Table 1 and Table 2 provide the recommended settings for the ADV7174/ADV7179.

Table 1.

Register (Hex)	Name	Default /Power Up Value	Recommend Value (hex)		Comment
			NTSC	PAL	
0	MR00	0x00	0x10	0x11	Extended Luma filter enabled
1	MR01	0x58	0x10		All 3 DACs enabled
2	MR02	0x00	0x00		Default value
3	MR03	0x00	0x08		
4	MR04	0x10	See Table 2		
5	Reserved	0x00	-		
6	Reserved	0x00	-		
7	T0	0x00	0x00 = EAV/SAV timing input 0x04 = HS/VS input		Default/Power up value Blank bit may need to be toggled depending on blank pin polarity
8	T1	0x00			
9	Fsc0	0x16	0x1F	0xCB	
A	Fsc1	0x7C	0x7C	0x8A	
B	Fsc2	0xF0	0xF0	0x09	
C	Fsc3	0x21	0x21	0x2A	

Additional registers can be left at their default values, unless the user requires additional features enabled by those registers. Refer to the datasheet for further information on additional features.

Table 2.

DAC Configuration	MR04
DAC A = Y DAC B = CVBS DAC C = C	0x10
DAC A = Y DAC B = Pb DAC C = Pr	0x13

REVISION HISTORY**10/13—Rev. 0 to Rev. A**

Added Header and Footer..... Universal
Introduction added 1
No changes to Table 1 and Table 2 contents

08/05 – Revision 0 : Initial Version**LEGAL TERMS AND CONDITIONS**

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