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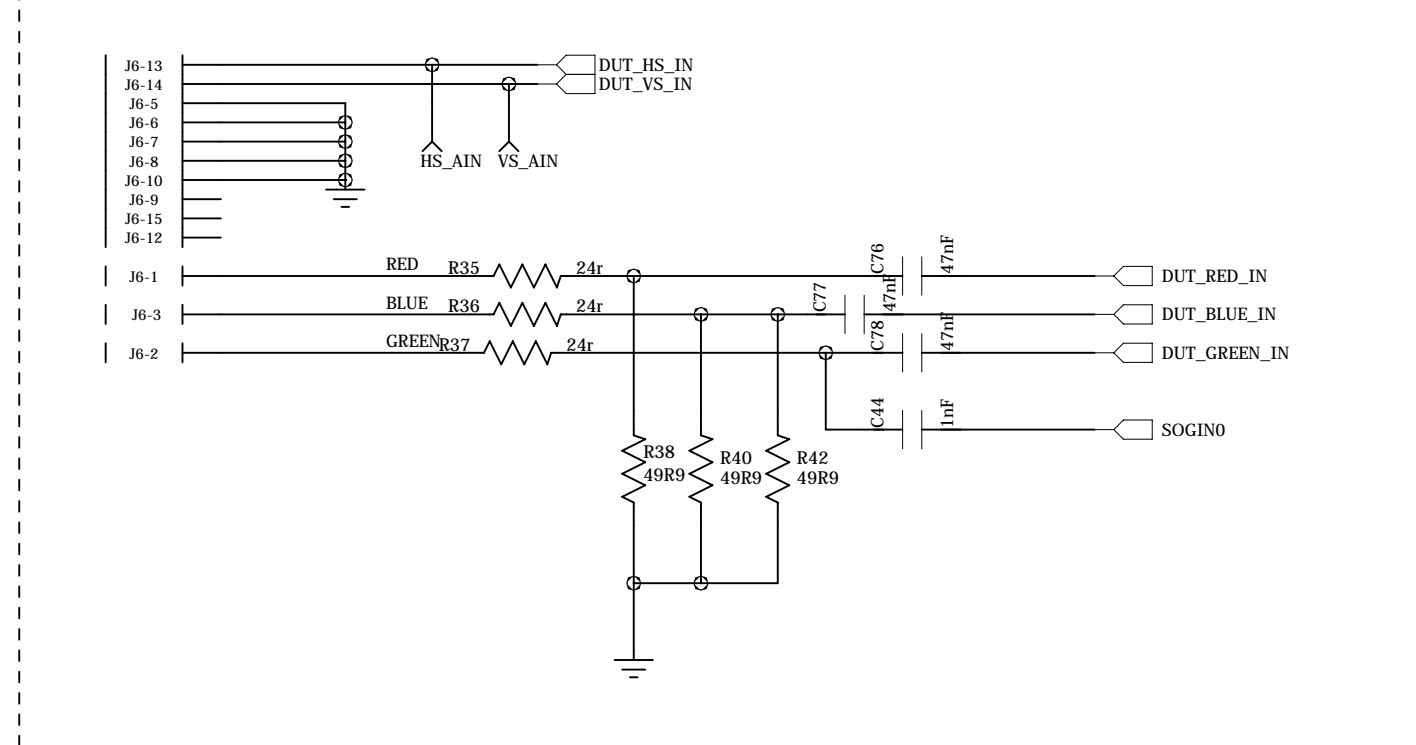
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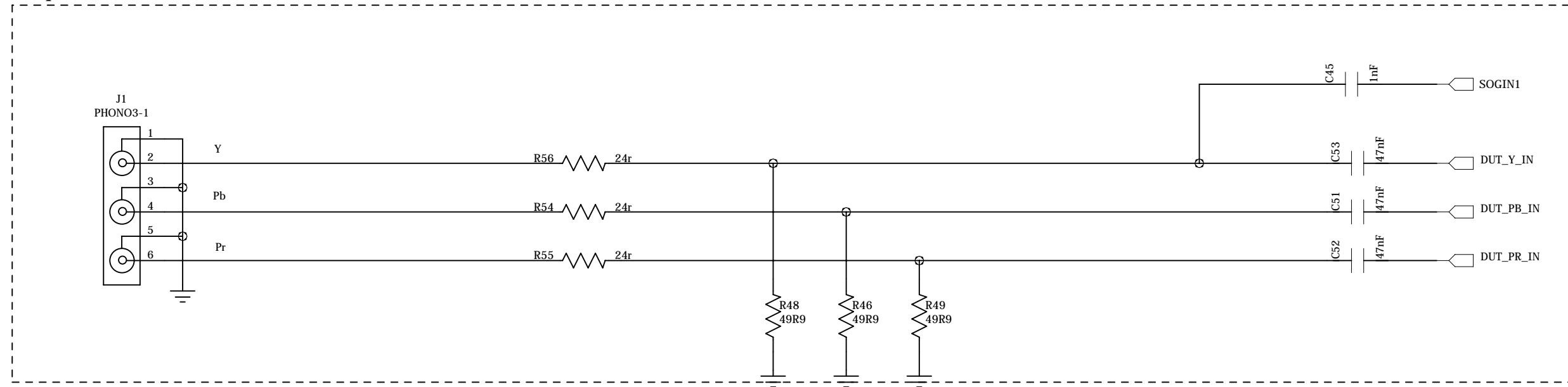
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

VGA Connector



Component Connector



Place analog inputs as far apart as possible while maintaining similar trace lengths
 Trace impedance of analog inputs to be 75 ohms
 Termination resistors should be placed as close as possible to end of line

COMPANY:				Analog Devices			
TITLE:				AD9984			
DRAWN: Graham Britchfield	DATED: June 2013	CODE:	SIZE:	DRAWING NO:	REV:		
CHECKED: <Checked By>	DATED: <Checked Date>	<Code>	A2	Rev. 0	0		
QUALITY CONTROL: <QC By>	DATED: <QC Date>	SCALE: <Scale>		SHEET: 1 OF 6			
RELEASED: <Released By>	DATED: <Release Date>						

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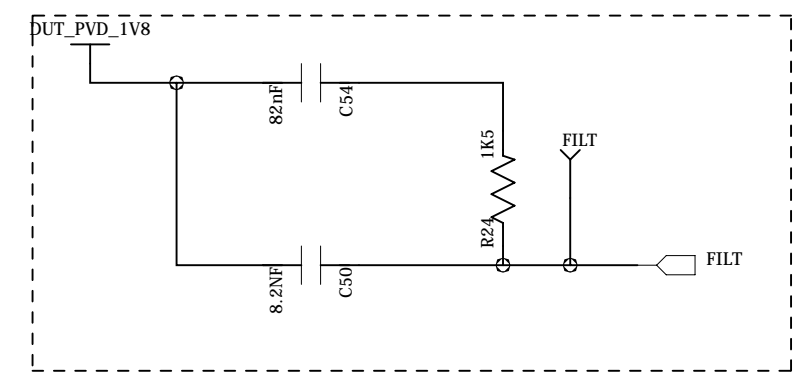
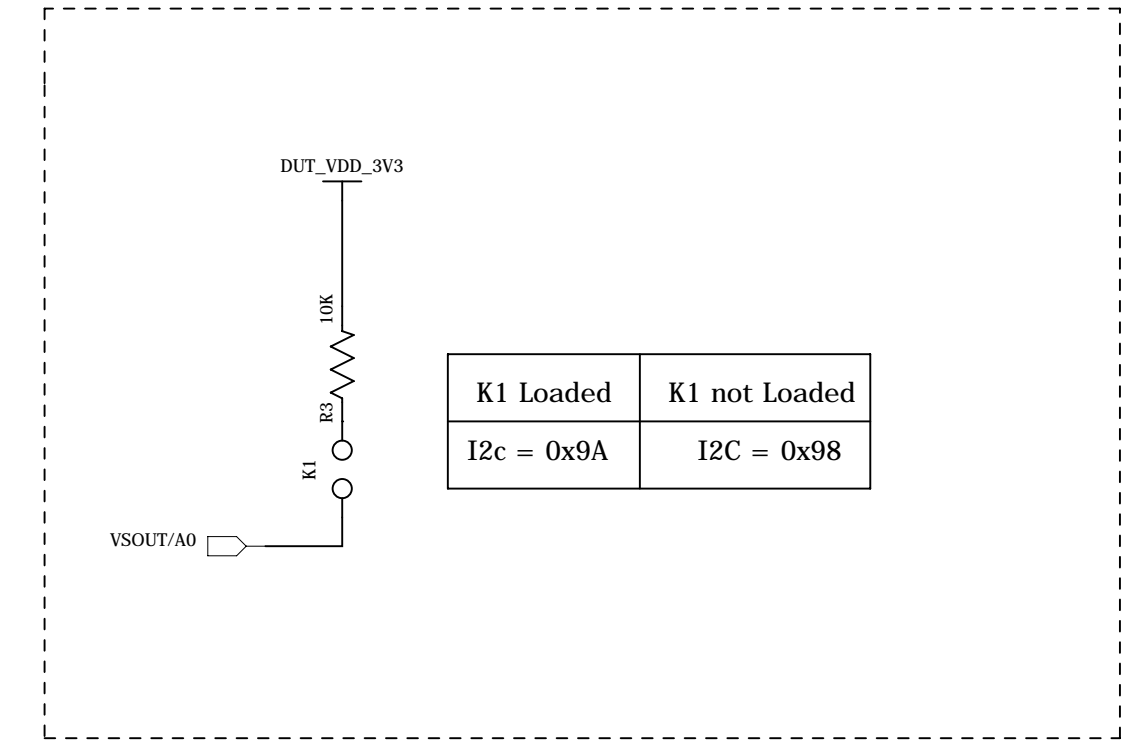
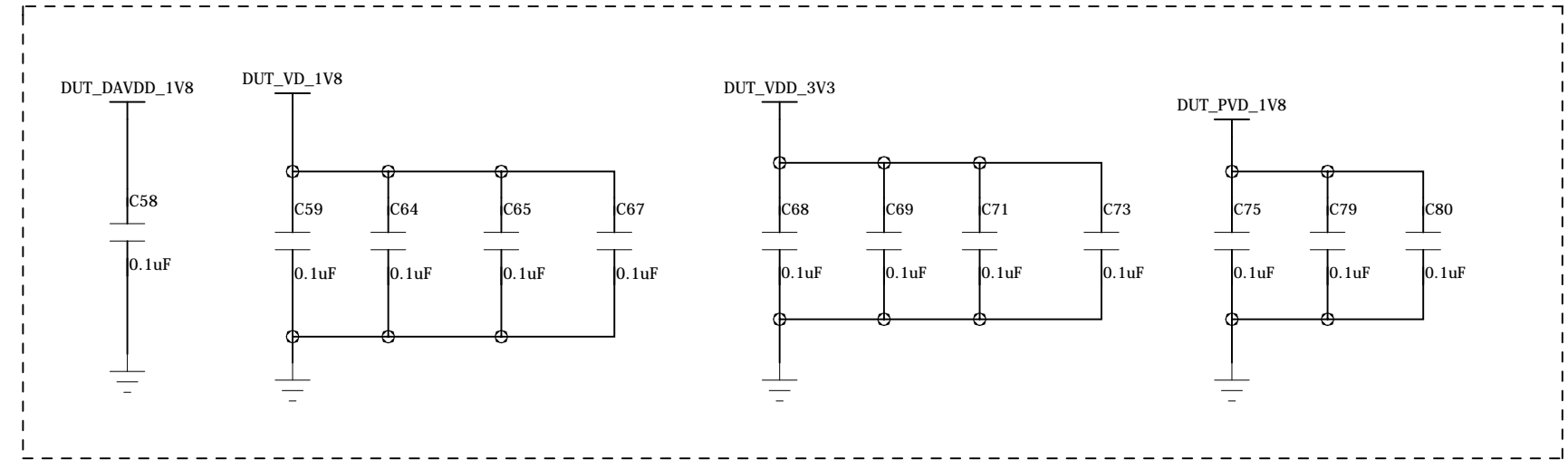
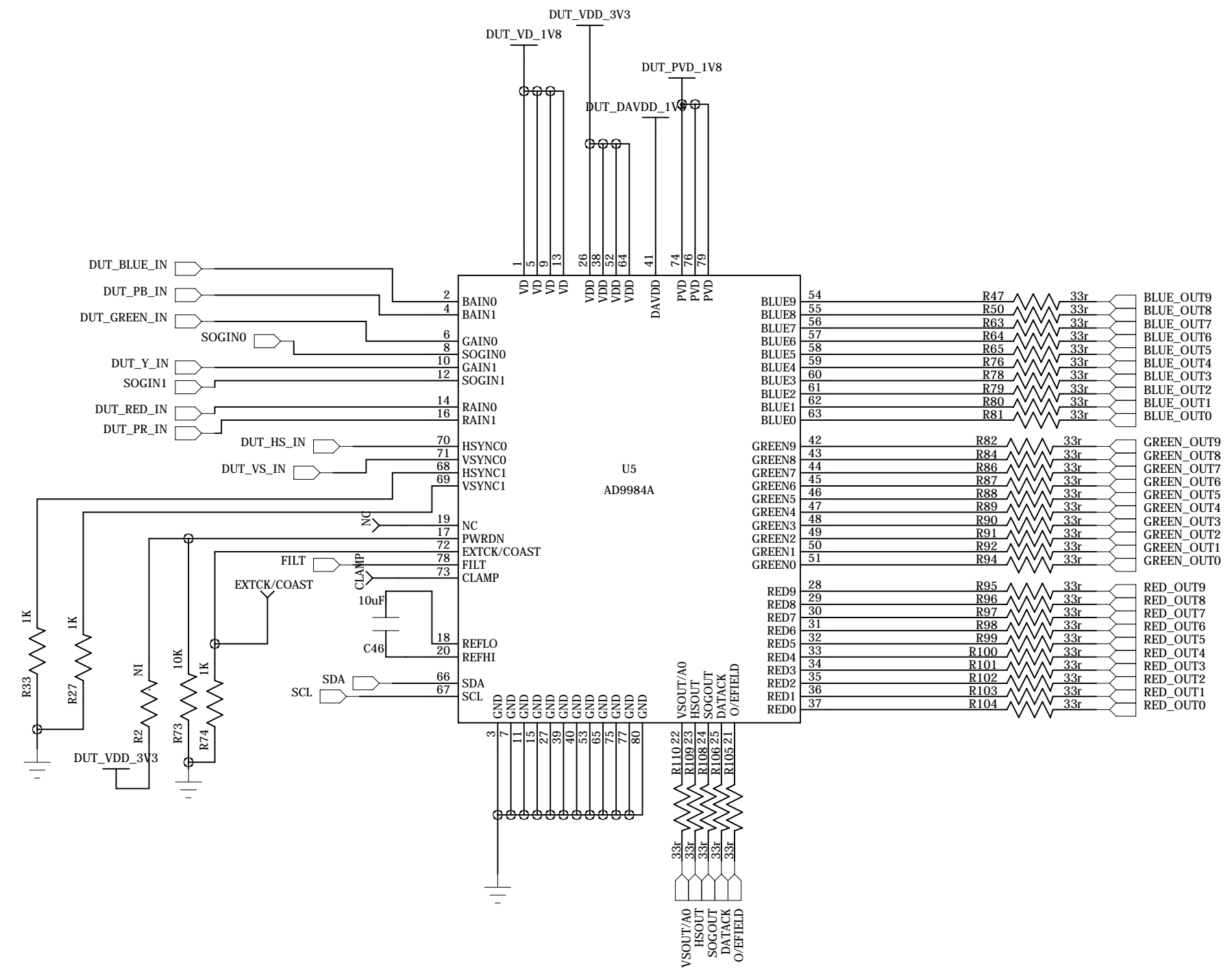
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY:				Analog Devices			
TITLE:				AD9984			
DRAWN: Graham Britchfield	DATED: June 2013	CODE:	SIZE:	DRAWING NO:	REV:		
CHECKED: <Checked By>	DATED: <Checked Date>	<Code>	A2	Rev. 0	0		
QUALITY CONTROL: <QC By>	DATED: <QC Date>						
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>		SHEET: 2 OF 6			

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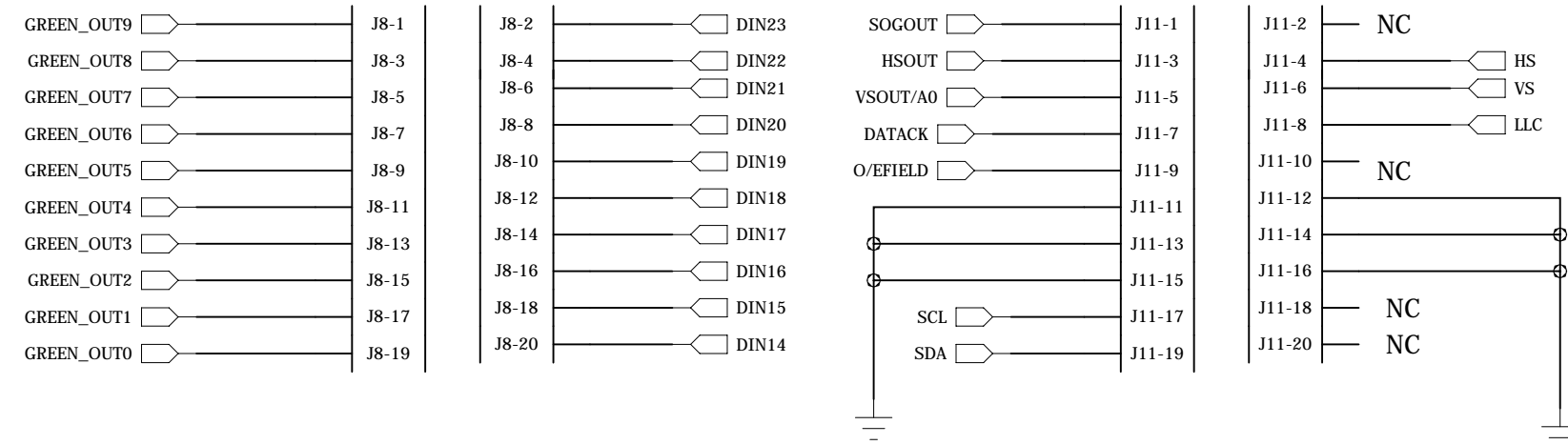
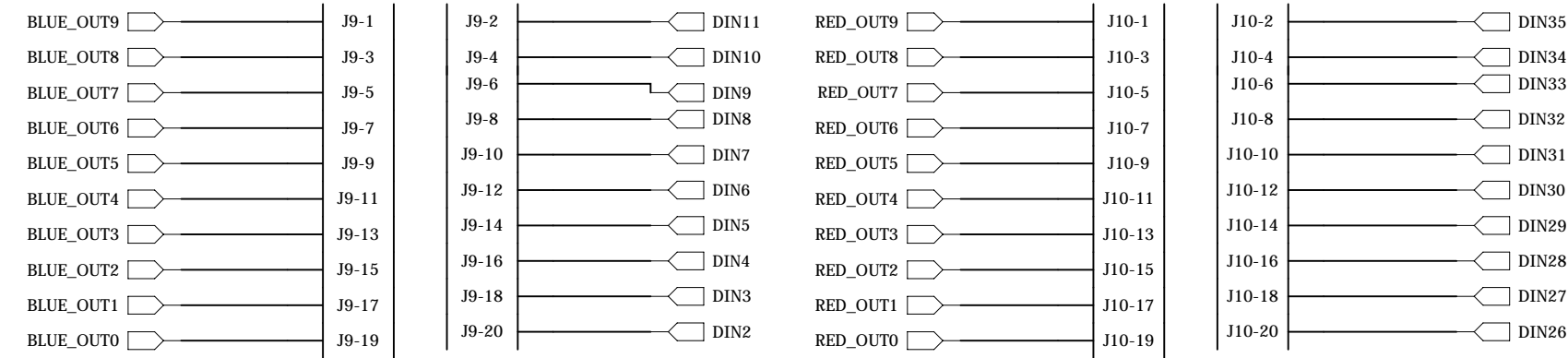
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



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DRAWN: Graham Britchfield		DATED: June 2013		COMPANY: Analog Devices			
CHECKED: <Checked By>		DATED: <Checked Date>		TITLE: AD9984			
QUALITY CONTROL: <QC By>	DATED: <QC Date>	CODE: <Code>	SIZE: A2	DRAWING NO: Rev. 0		REV: 0	
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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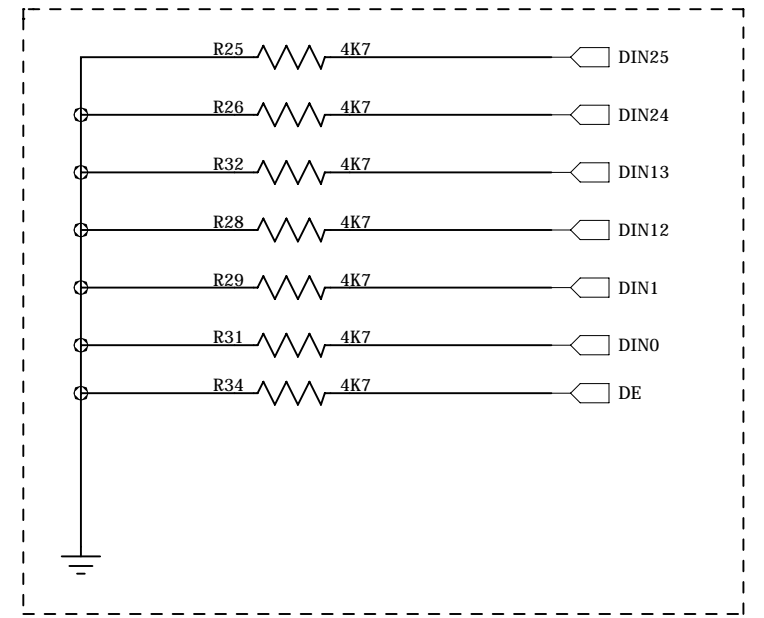
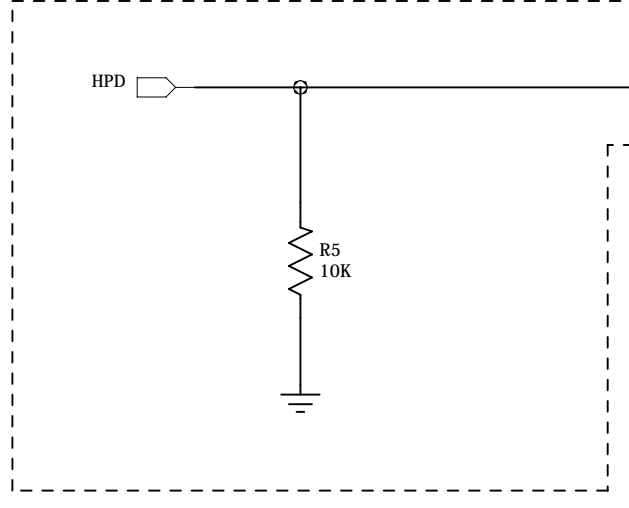
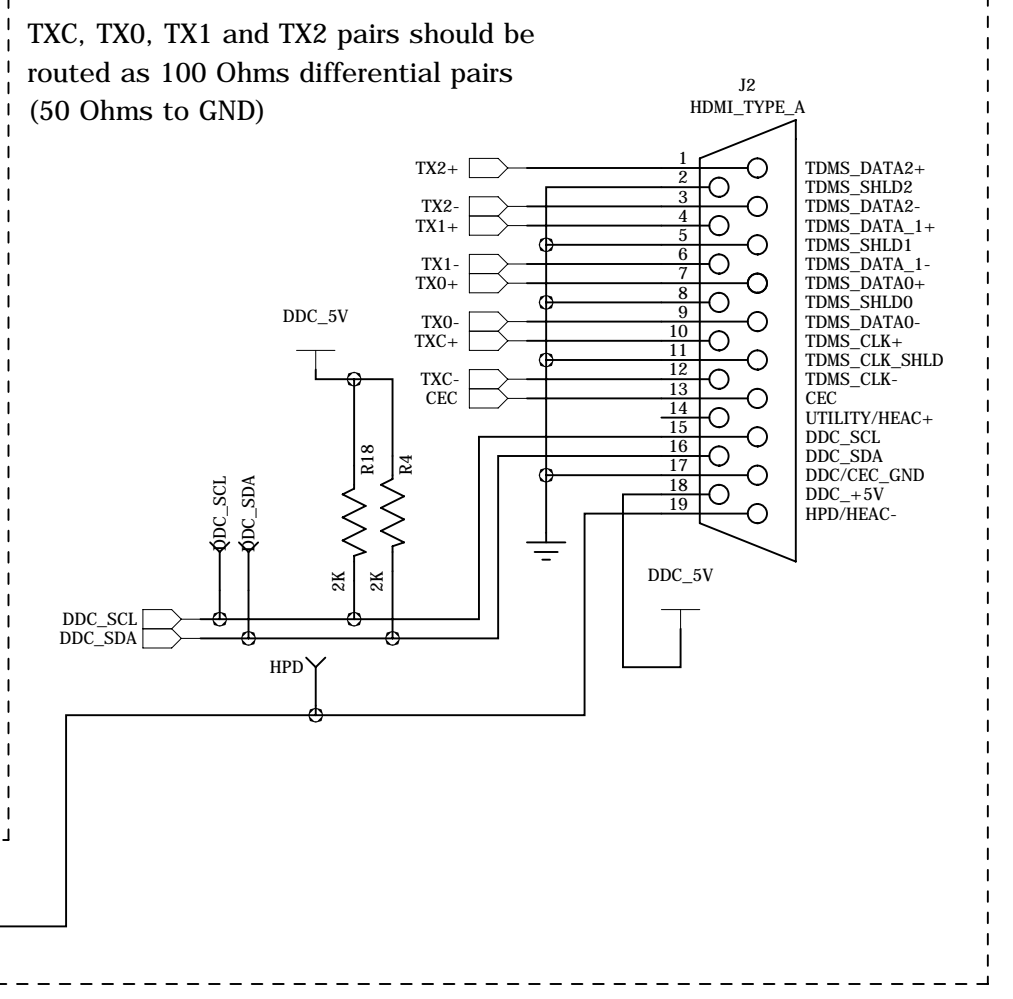
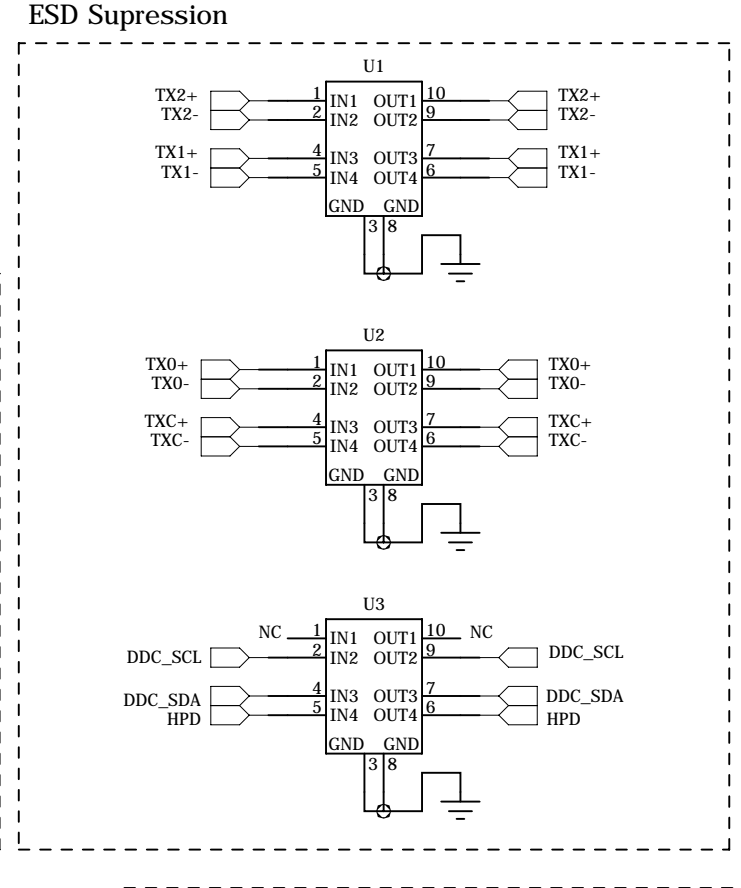
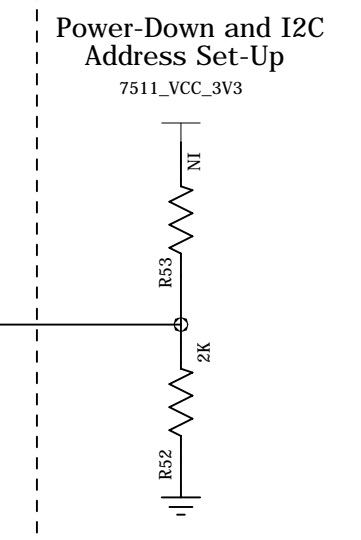
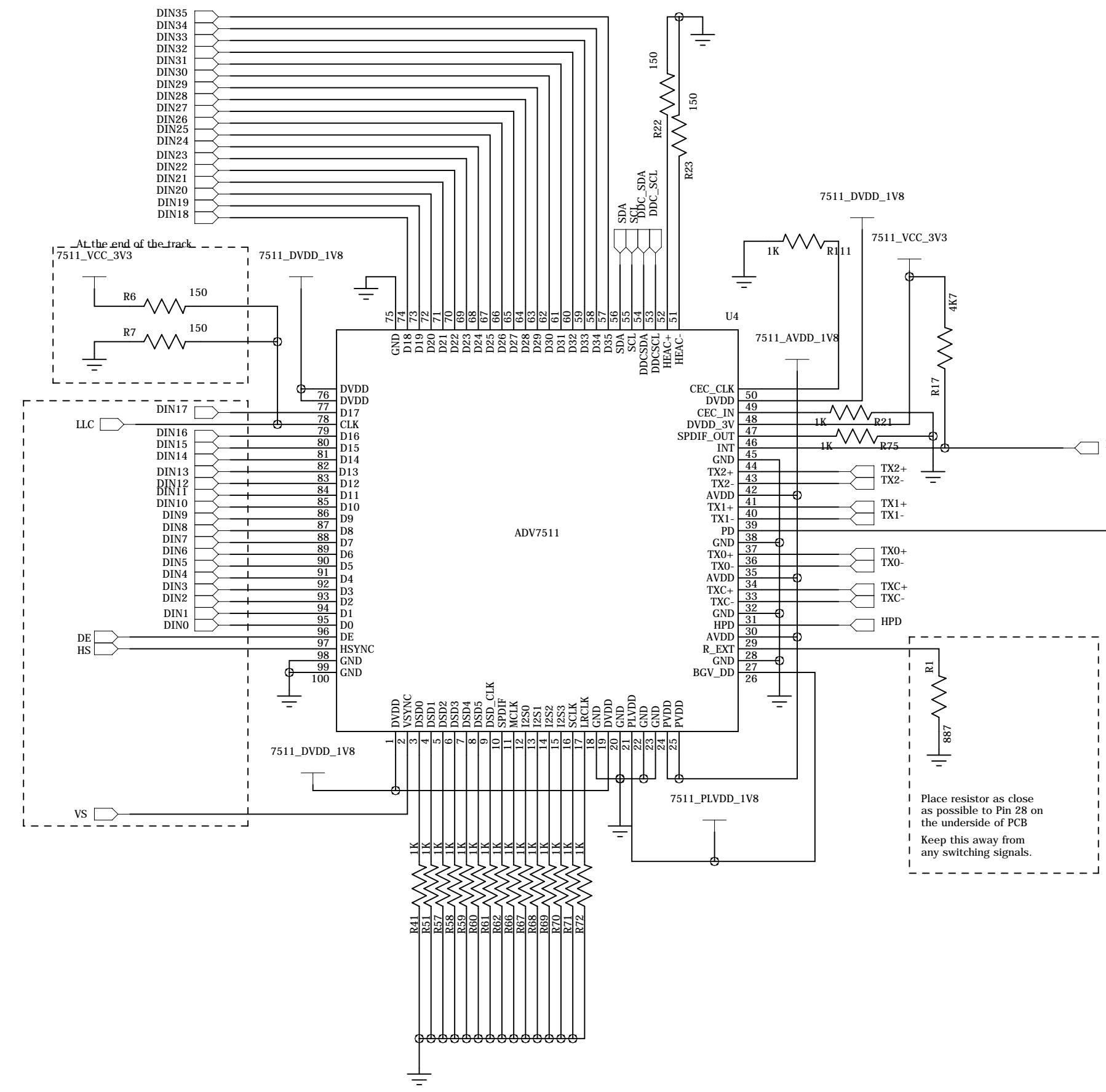
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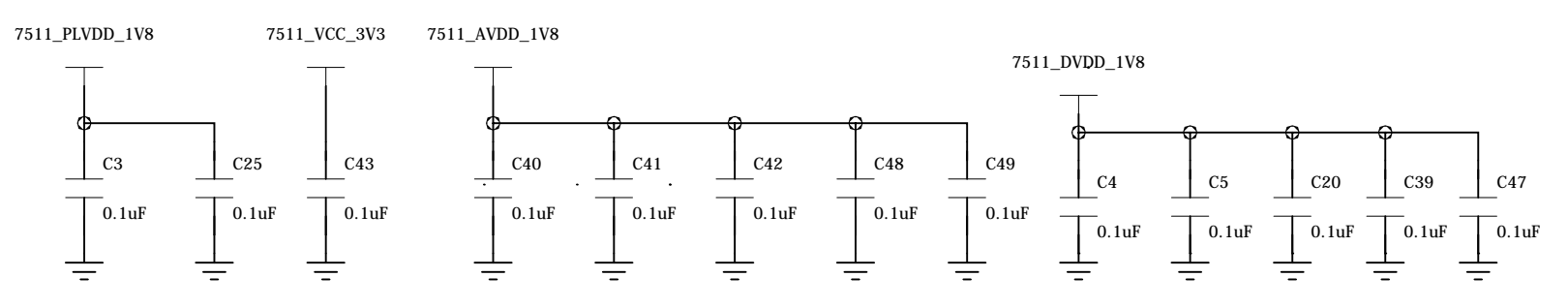
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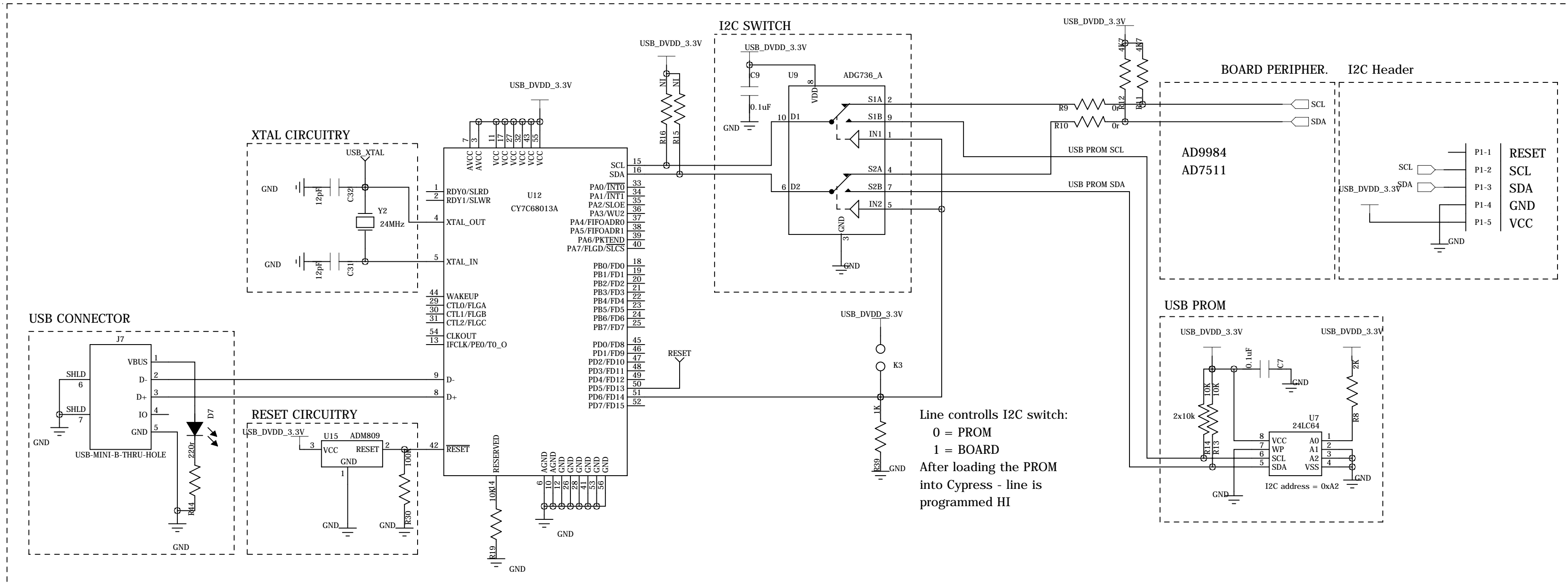
ADV7511 Bypass



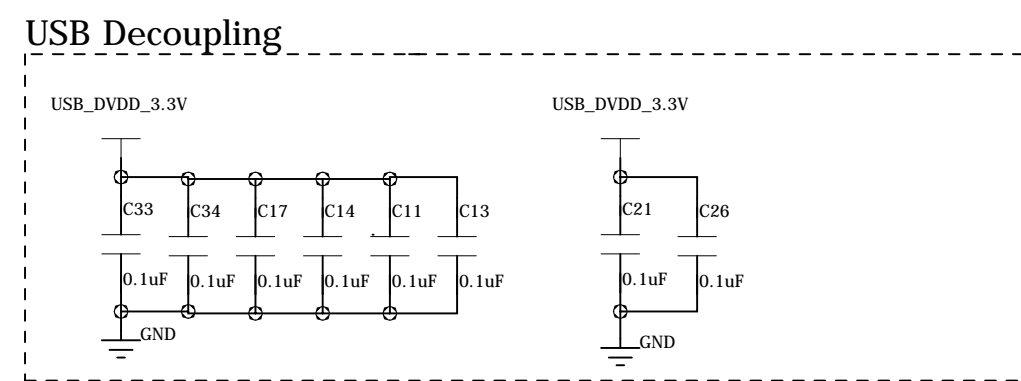
COMPANY: Analog Devices			
TITLE: AD9984			
CODE:	SIZE: A2	DRAWING NO:	REV: 0
SCALE:		SHEET: 4 OF 6	

DRAWN: Graham Britchfield	DATED: June 2013
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Line controls I2C switch:
 0 = PROM
 1 = BOARD
 After loading the PROM into Cypress - line is programmed HI



COMPANY: Analog Devices			
TITLE: AD9984			
DRAWN: Graham Britchfield	DATED: June 2013	CODE:	SIZE: A2
CHECKED: <Checked By>	DATED: <Checked Date>	<Code>	DRAWING NO: Rev. 0
QUALITY CONTROL: <QC By>	DATED: <QC Date>		
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	SHEET: 5 OF 6

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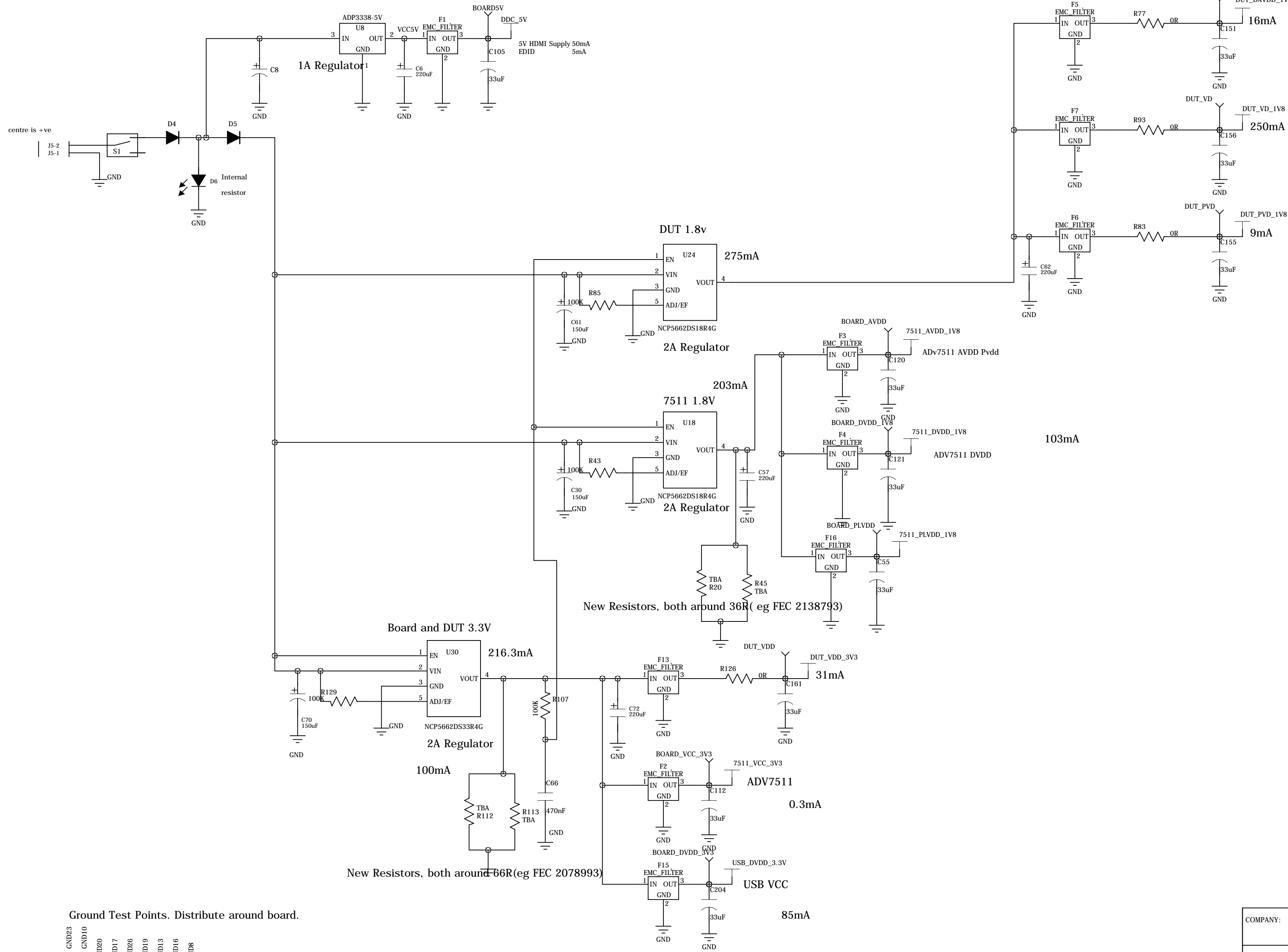
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Power Page

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



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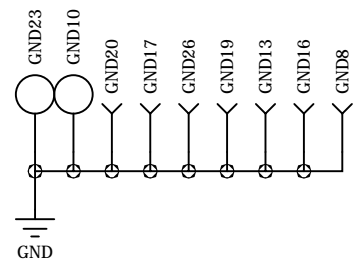
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Ground Test Points. Distribute around board.



COMPANY: Analog Devices			
TITLE: AD9984			
DRAWN: Graham Britchfield	DATED: June 2013	CODE: <Code>	SIZE: A2
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO: Rev. 0	REV: 0
QUALITY CONTROL: <QC By>	DATED: <QC Date>	SCALE: <Scale>	SHEET: 6 OF 6
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