

ADV734X and ADV739X Video Encoders																			
#Part	# DAC	Resolution (Bits)	Input Data Resolution	Total Number of Pixel Inputs	Max Oversampling (MHz)	Number of on-chip PLLs	Reset Pin	Software Reset	Power on Reset	Macrovision Compliant	Simultaneous SD and ED Operation	SD Digital RGB Input	Prog Scan	HDTV	Minimum Temperature Range	Maximum Temperature Range	I/O Supply (V)	Package Footprint	Package
Query Parameter																			
ADV7340	6	12bit	10bit	30	297MHz	2	No	Yes	Yes	Yes	Yes	Yes	Yes	Yes	-40C	+85C	1.8V to 3.3V	10X10 (mm)	64-Lead LQFP
ADV7341	6	12bit	10bit	30	297MHz	2	No	Yes	Yes	No	Yes	Yes	Yes	Yes	-40C	+85C	1.8V to 3.3V	10X10	64-Lead LQFP
ADV7342	6	11bit	8bit	24	297MHz	2	No	Yes	Yes	Yes	Yes	Yes	Yes	Yes	-40C	+85C	1.8V to 3.3V	10X10	64-Lead LQFP
ADV7343	6	11bit	8bit	24	297MHz	2	No	Yes	Yes	No	Yes	Yes	Yes	Yes	-40C	+85C	1.8V to 3.3V	10X10	64-Lead LQFP
ADV7344	6	14bit	10bit	30	297MHz	2	No	Yes	Yes	Yes	Yes	Yes	Yes	Yes	-40C	+85C	1.8V to 3.3V	10X10	64-Lead LQFP
ADV7390	3	10bit	8bit	8	297MHz	1	Yes	Yes	No	Yes	No	No	Yes	Yes ¹	-40C	+85C	1.8V to 3.3V	5X5	32-LFCSP
ADV7391	3	10bit	8bit	8	297MHz	1	Yes	Yes	No	No	No	No	Yes	Yes ¹	-40C	+85C	1.8V to 3.3V	5X5	32-LFCSP
ADV7392	3	10bit	10bit	16	297MHz	1	Yes	Yes	No	Yes	No	Yes	Yes	Yes	-40C	+85C	1.8V to 3.3V	6X6	40-LFCSP
ADV7393	3	10bit	10bit	16	297MHz	1	Yes	Yes	No	No	No	Yes	Yes	Yes	-40C	+85C	1.8V to 3.3V	6X6	40-LFCSP
ADV7390BCBZ-A-RL	1	10bit	8bit	8	297MHz	1	Yes	Yes	No	Yes	No	No	No	No	-40C	+105C	1.8V to 3.3V	3X3	30-WLCSP
ADV7390W	3	10bit	8bit	8	297MHz	1	Yes	Yes	No	Yes	No	No	Yes	Yes ¹	-40C	+105C	1.8V to 3.3V	5X5	32-LFCSP
ADV7391W	3	10bit	8bit	8	297MHz	1	Yes	Yes	No	No	No	No	Yes	Yes ¹	-40C	+105C	1.8V to 3.3V	5X5	32-LFCSP
ADV7392W	3	10bit	10bit	16	297MHz	1	Yes	Yes	No	Yes	No	Yes	Yes	Yes	-40C	+105C	1.8V to 3.3V	6X6	40-LFCSP
ADV7393W	3	10bit	10bit	16	297MHz	1	Yes	Yes	No	No	No	Yes	Yes	Yes	-40C	+105C	1.8V to 3.3V	6X6	40-LFCSP
¹ Supported through DDR only																			