

- *void ADV800x_ExtOSD_SetFormat(VID_FORMAT_SEL format)*
- *void ADV800x_ExtOSD_SetSawpCbCrOrder(SWAP_CB_CR_422 val)*
- *void ADV800x_ExtOSD_SetCSC(BOOL CscEn)*

- ii. Video TTL
 - *void ADV800x_VIDTTL_SetFormat(VID_FORMAT_SEL format)*
 - *void ADV800x_VIDTTL_SetSawpCbCrOrder(SWAP_CB_CR_422 val)*
 - *void ADV800x_VIDTTL_SetCSC(BOOL CscEn)*

- iii. HDMI RX
 - *void ADV800x_RX_SetFormat(VID_FORMAT_SEL format)*
 - *void ADV800x_RX_SetSawpBusCtrl(VID_SWAP_BUS val)*
 - *void ADV800x_RX_SetCSC(BOOL CscEn)*

2.4.2 DDR2 external OSD address configuration

2.5 Audio

2.5.1 Insertion configuration

- *ATV_ERR_TXHAL_SetAudioChannel(UCHAR Channel)*
 - **Parameters:**
Channel = 0: from external audio pin
Channel = 1: from internal (HDMI RX)

2.5.2 Tx ARC

- *ATV_ERR_TXHAL_ArcSetMode(TX_ARC_MODE ArcMode)*
 - **Parameters:**
typedef enum
{
TX_ARC_OFF = 0,
TX_ARC_SINGLE,
TX_ARC_COMMON
} TX_ARC_MODE;

2.6 TTL pin configuration

- *void ADV800x_TTLOUT_Enable(BOOL OutEn)*