

ADV7613

**Register Settings
Recommendations**

Revision A

February 2016

About This Document

This document describes ADI register setting recommendations and adjustments for the ADV7613 product. This document must be used in conjunction with the latest Device Manual User Guide and the Register Control Manual.

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The contents of this document are believed to be correct. If errors within this document are found or clarifications are needed, contact the author: AV-Video Applications, who will respond as soon as possible.

Revision History

Revision	Date	Changes
A	02/2016	HDMI Register Settings section
0	10/2015	Initial

Table of Contents

1. Introduction.....	4
2. ADV7613 I2C Addresses	4
3. CP Register Settings.....	4
4. HDMI Register Settings.....	4
5. OpenLDI Register Settings.....	5

List of Tables

No table of figures entries found.

List of Figures

No table of figures entries found.

1. Introduction

This document details the recommended settings used in the ADV7613. It is advised that this document be used in conjunction with the latest scripts and Device Manual User Guide and the Register Control Manual.

2. ADV7613 I2C Addresses

The ADV7613 contains 8 I²C register maps which can be accessed through the main I²C port. In ADV7613 each map has its own I²C address and acts as a standard slave device on the I²C bus.

These writes are listed below with possible addresses. When referencing an I²C Map in this document, the sample I²C address values are used throughout this document.

SLAVE ADDRESSES

98 F4 80	CEC Map Address
98 F5 7C	Infoframe Map Address
98 F8 4C	DPLL Map Address
98 F9 64	KSV Map Address
98 FA 6C	EDID Map Address
98 FB 68	HDMI Map Address
98 FD 44	CP Map Address
98 E9 C0	LVDS Map Address

3. CP Register Settings

ADI recommends that the register setting below should be programmed to setup the ADV7613.

CP MAP

44 6C 00	ADI recommended write
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4. HDMI Register Settings

ADI recommends that the register settings below should be programmed to setup the ADV7613 in all TMDS clock frequency operation.

HDMI Map

68 03 98	ADI Recommended write
68 10 A5	ADI Recommended write
68 1B 08	ADI Recommended write
68 45 04	ADI Recommended write
68 97 C0	ADI Recommended write
68 3D 10	ADI Recommended write
68 3E 7B	ADI recommended write

68 3F 5E	ADI Recommended Write
68 4E FE	ADI recommended write
68 4F 08	ADI recommended write
68 57 A3	ADI recommended write
68 58 07	ADI recommended write
68 6F 08	ADI Recommended write
68 83 FE	ADI recommended write
68 86 9B	ADI recommended write
68 89 01	ADI recommended write
68 9B 03	ADI recommended write
68 9C 80	ADI Recommended write
68 9C C0	ADI Recommended write
68 9C 00	ADI Recommended write

HDMI: – TMDS INPUT CLOCK FREQUENCY 25 to 40 MHz OPERATION

68 85 11	ADI Recommended write
68 93 03	ADI Recommended write
68 5A 80	ADI Recommended write

HDMI: – TMDS INPUT CLOCK FREQUENCY GREATER THAN 40 MHz OPERATION

68 85 10	ADI Recommended write
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5. OpenLDI Register Settings

ADI recommends that the register setting below should be programmed to setup the ADV7613.

LVDS Map

C0 43 03	ADI Recommended write
C0 45 04	ADI Recommended write
C0 46 53	ADI Recommended write
C0 47 03	ADI Recommended write